

Figure 1A (Prior Art)

20f 51

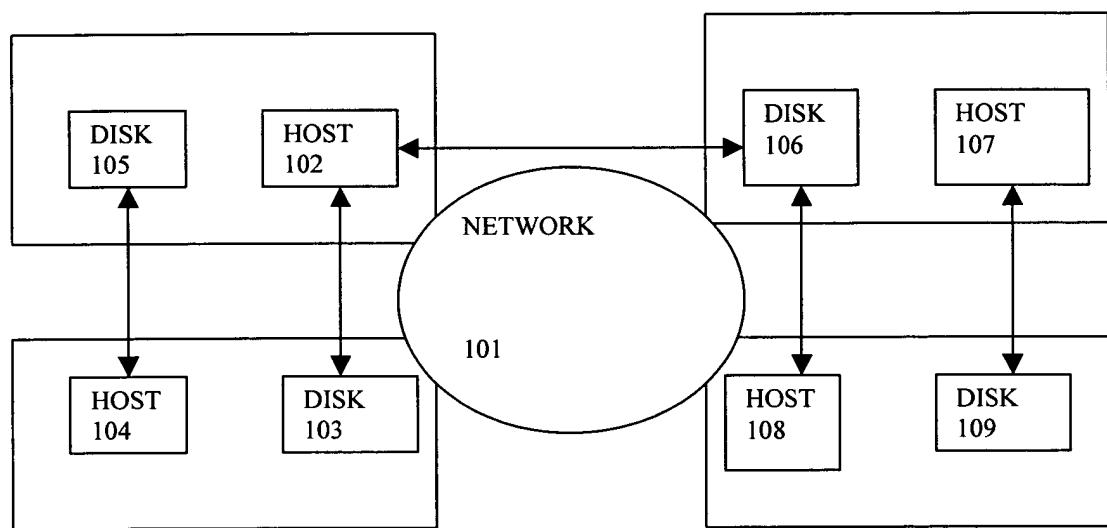


FIGURE 2A

3 of 5)

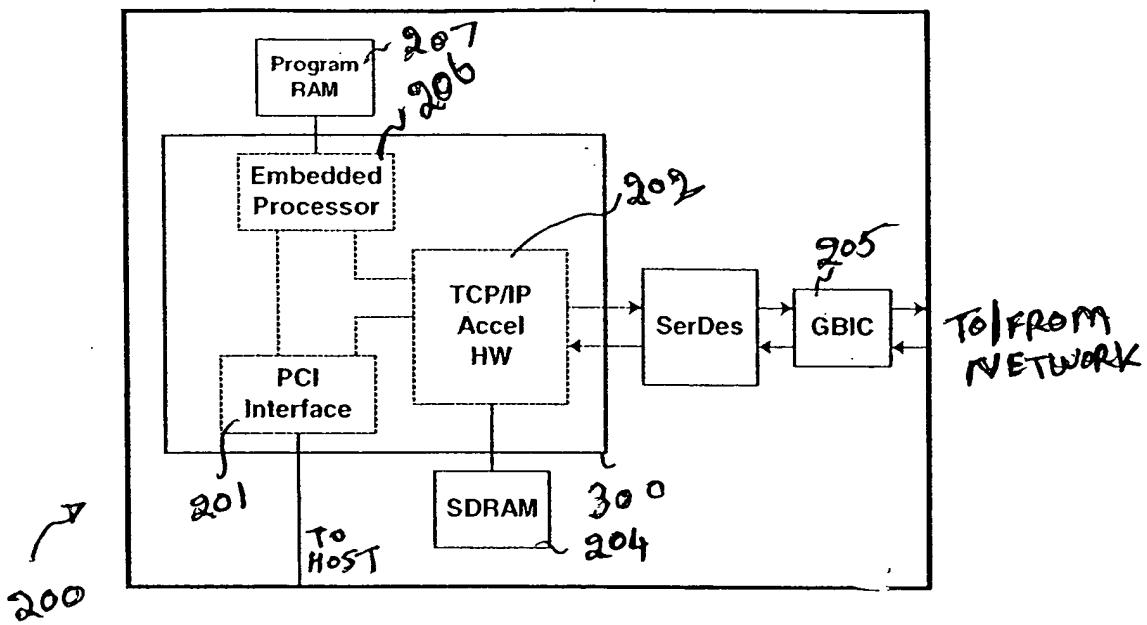


Figure 2B

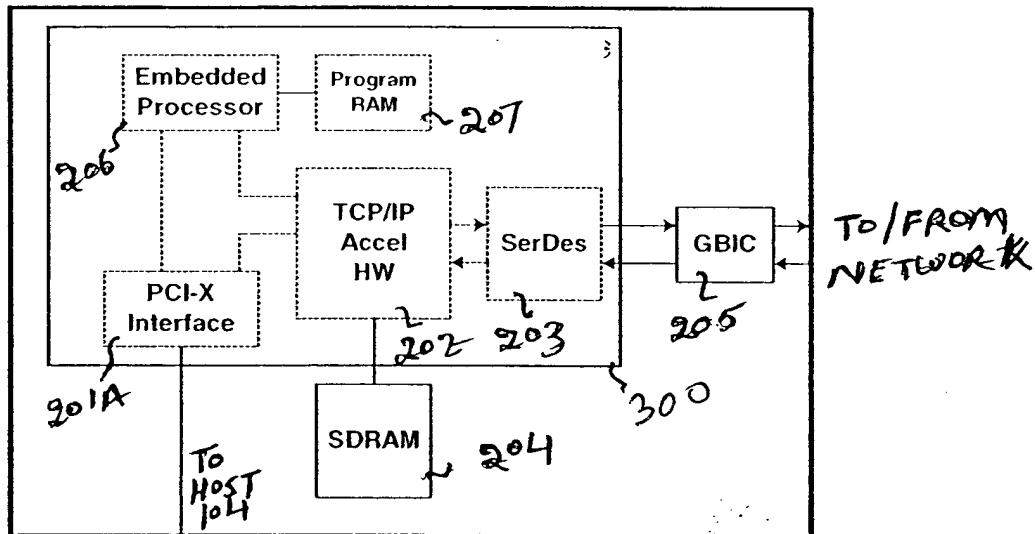


Figure 2C

200A

4 af5

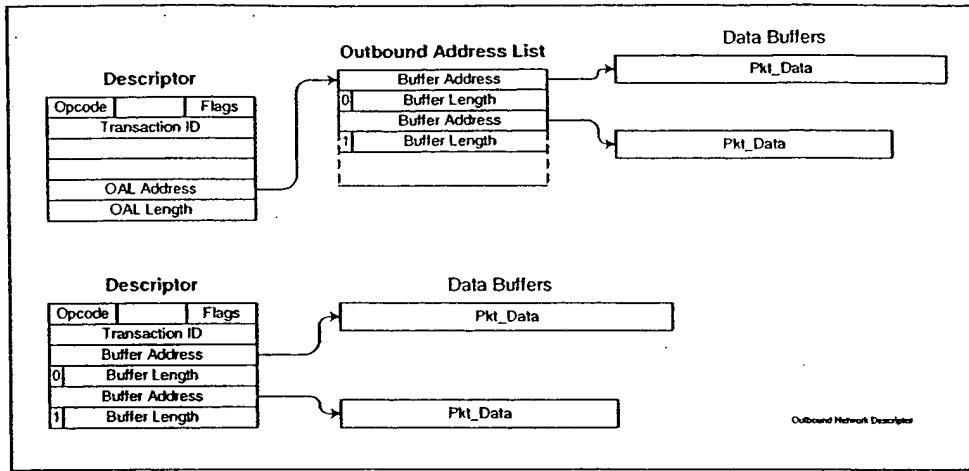
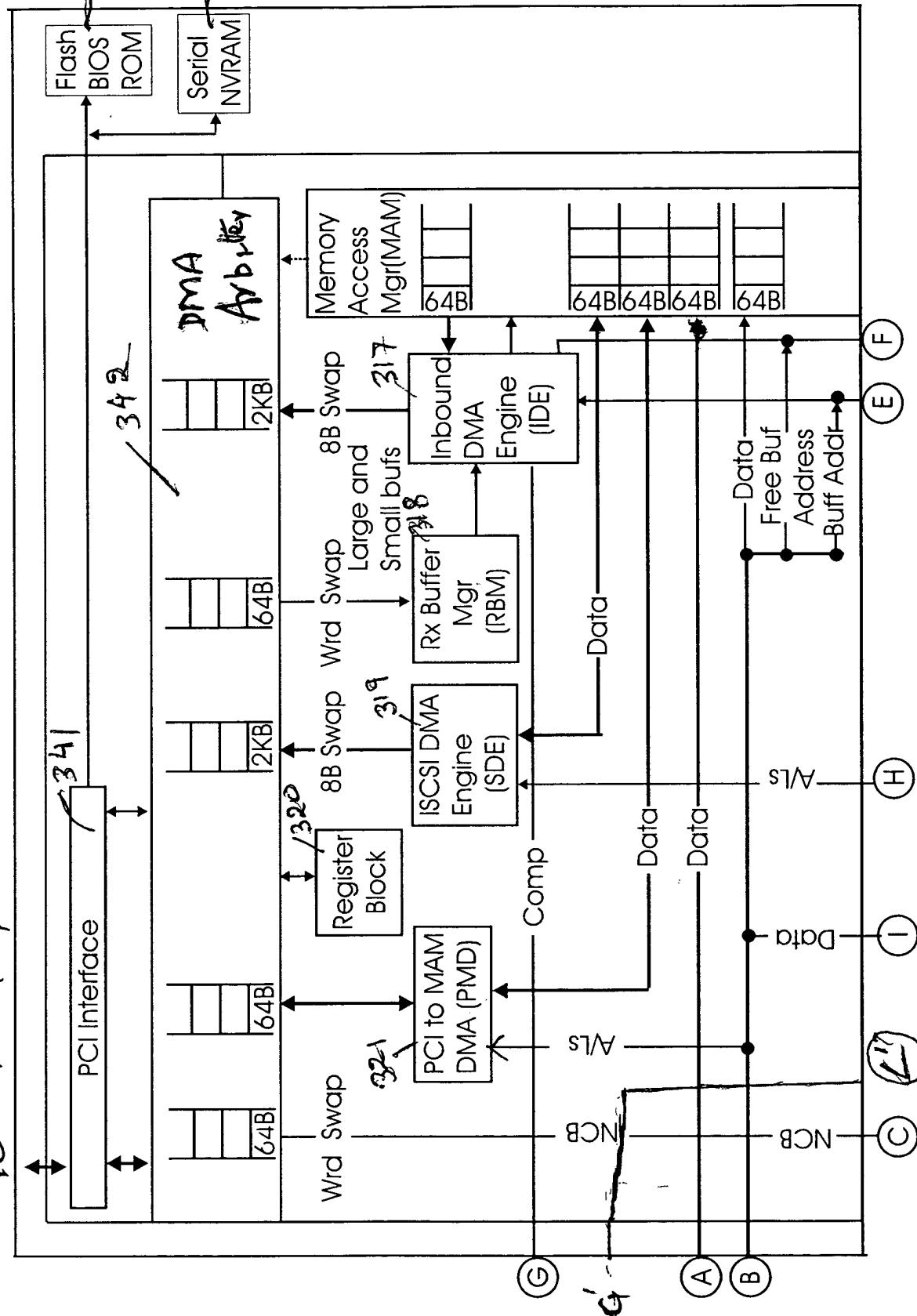


Figure 3A-1 Network IOCB Example

To Host 104



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FIGURE 3A1-4

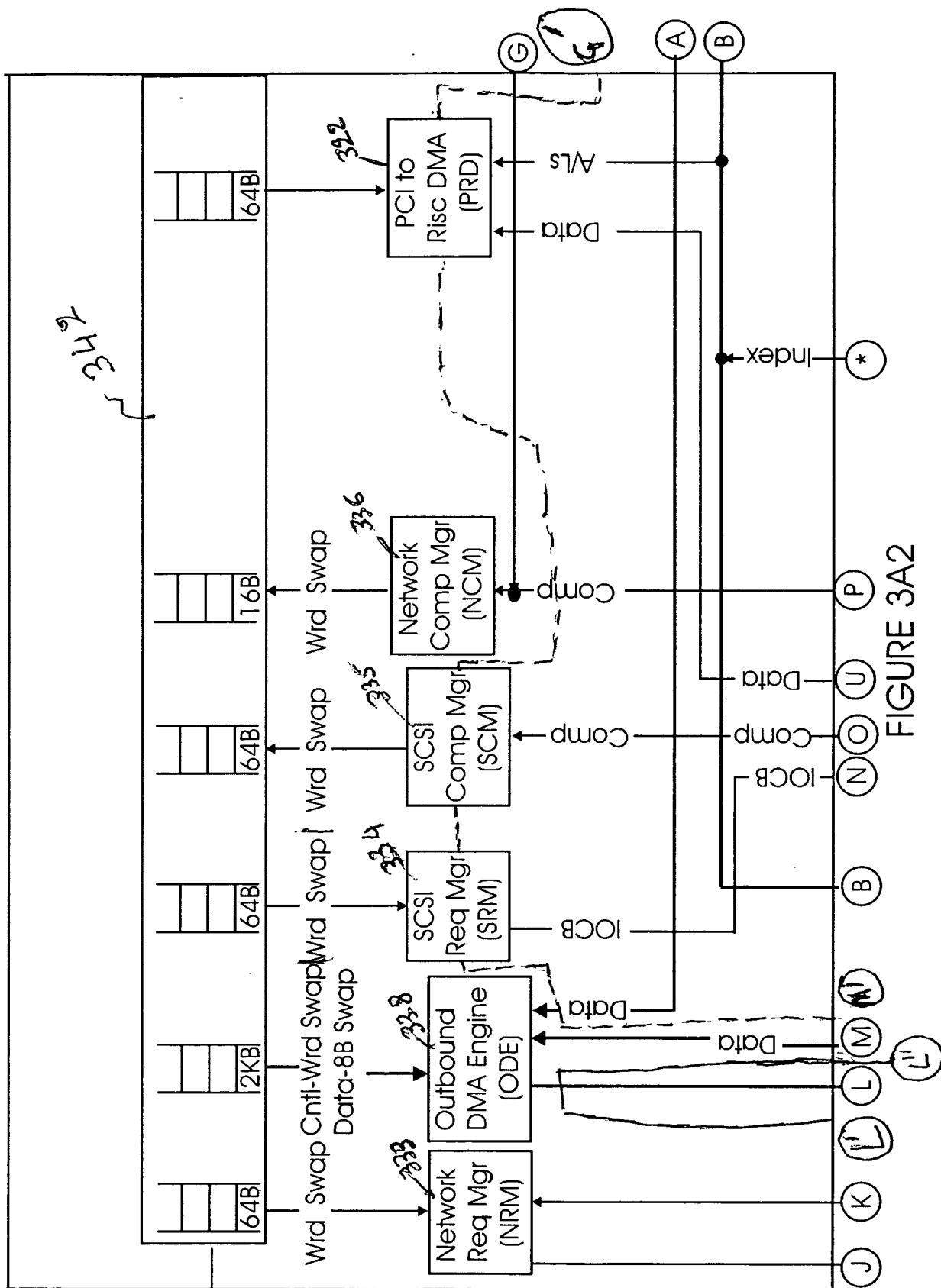
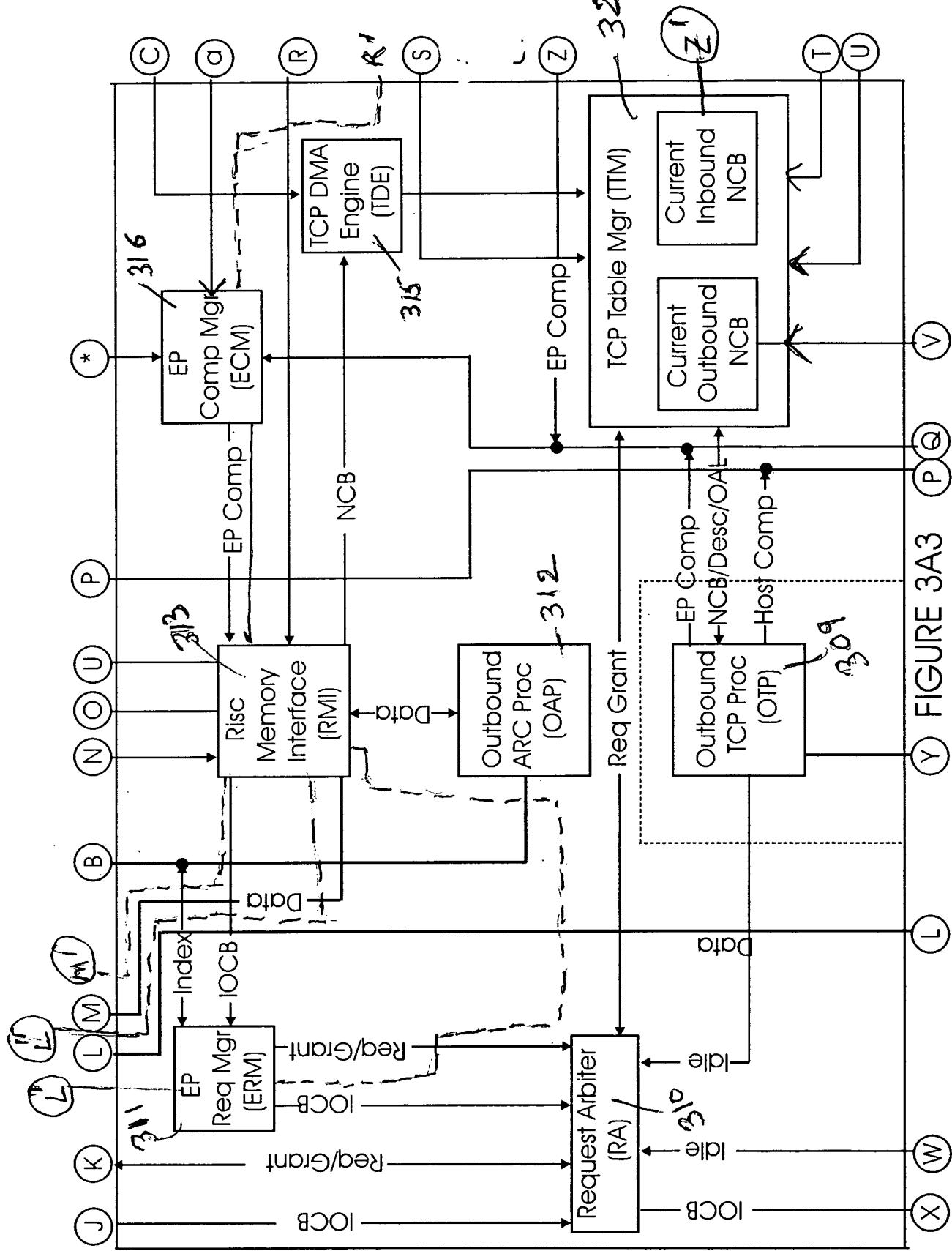


FIGURE 3A2



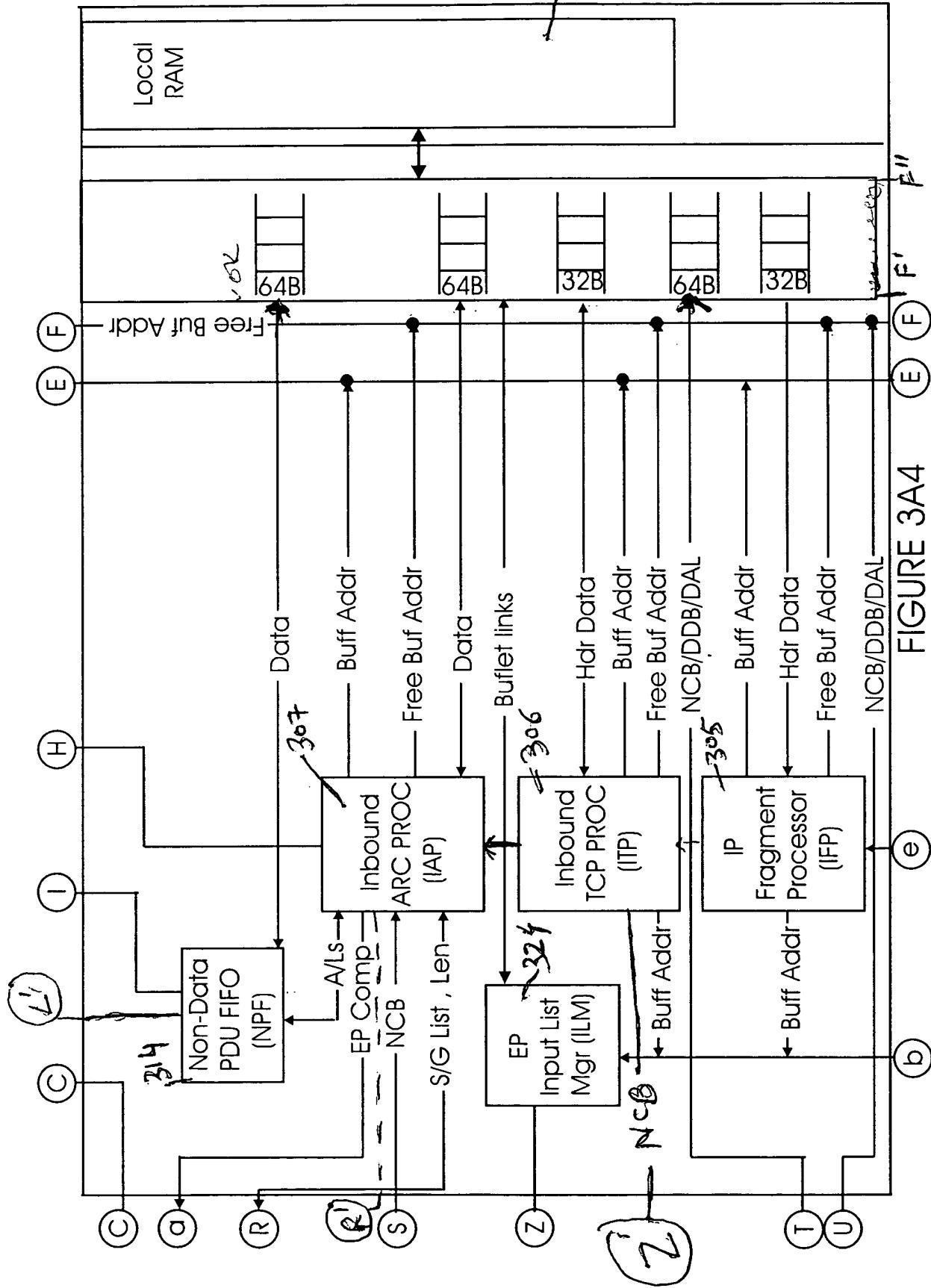


FIGURE 3A4

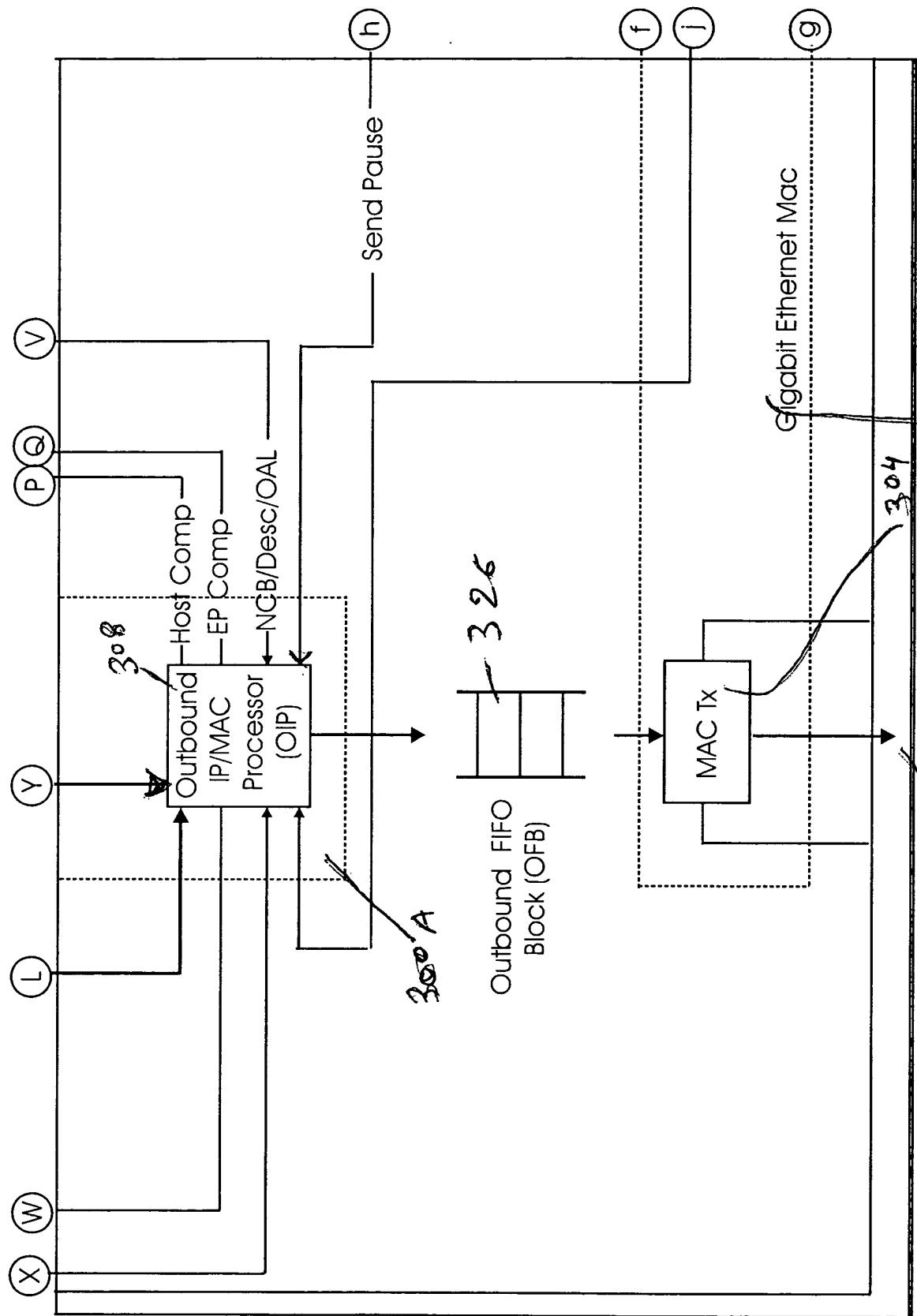
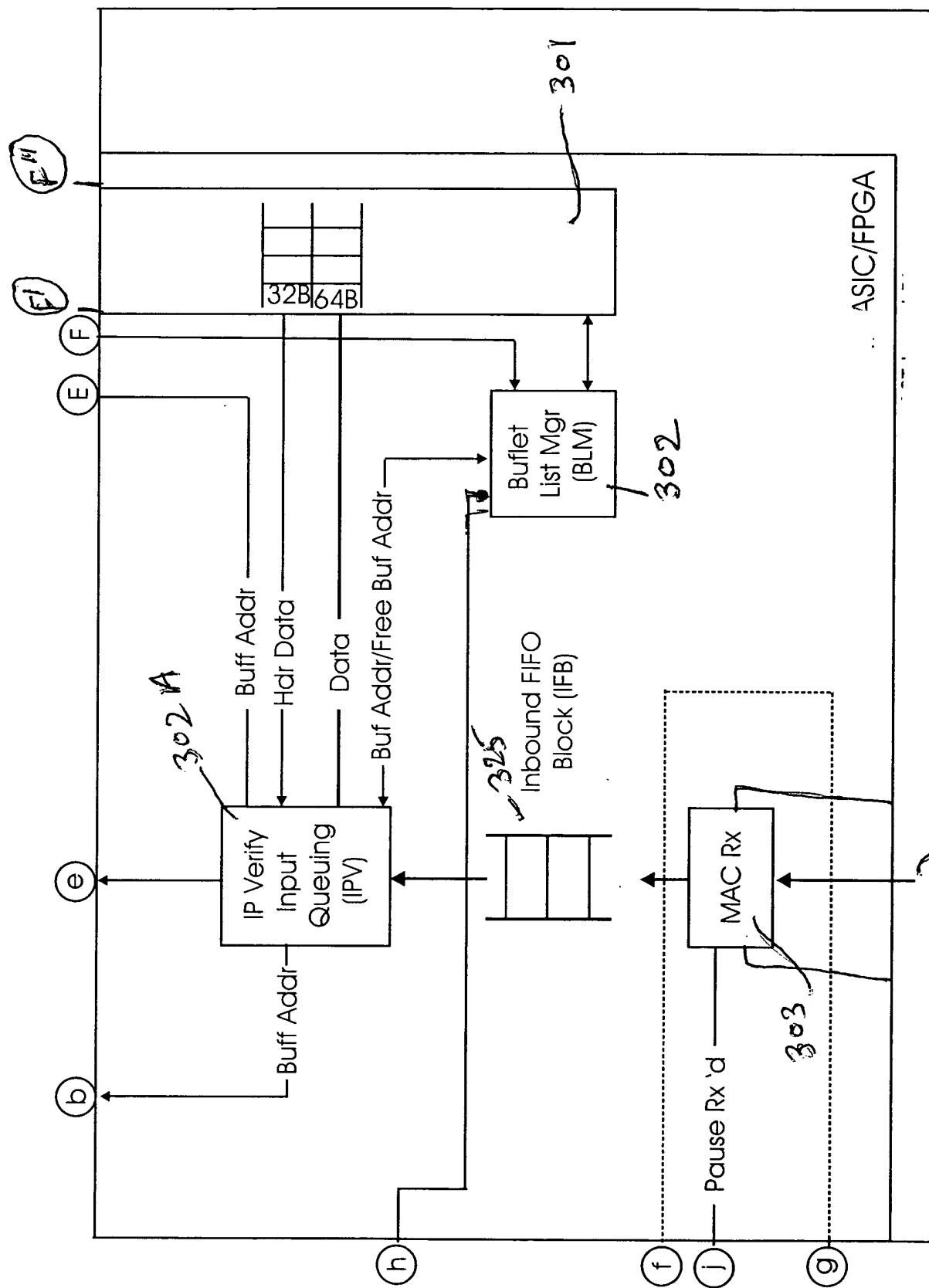


FIGURE 3A5
303 B
304 A

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331 FIGURE 3A6

11 of 51

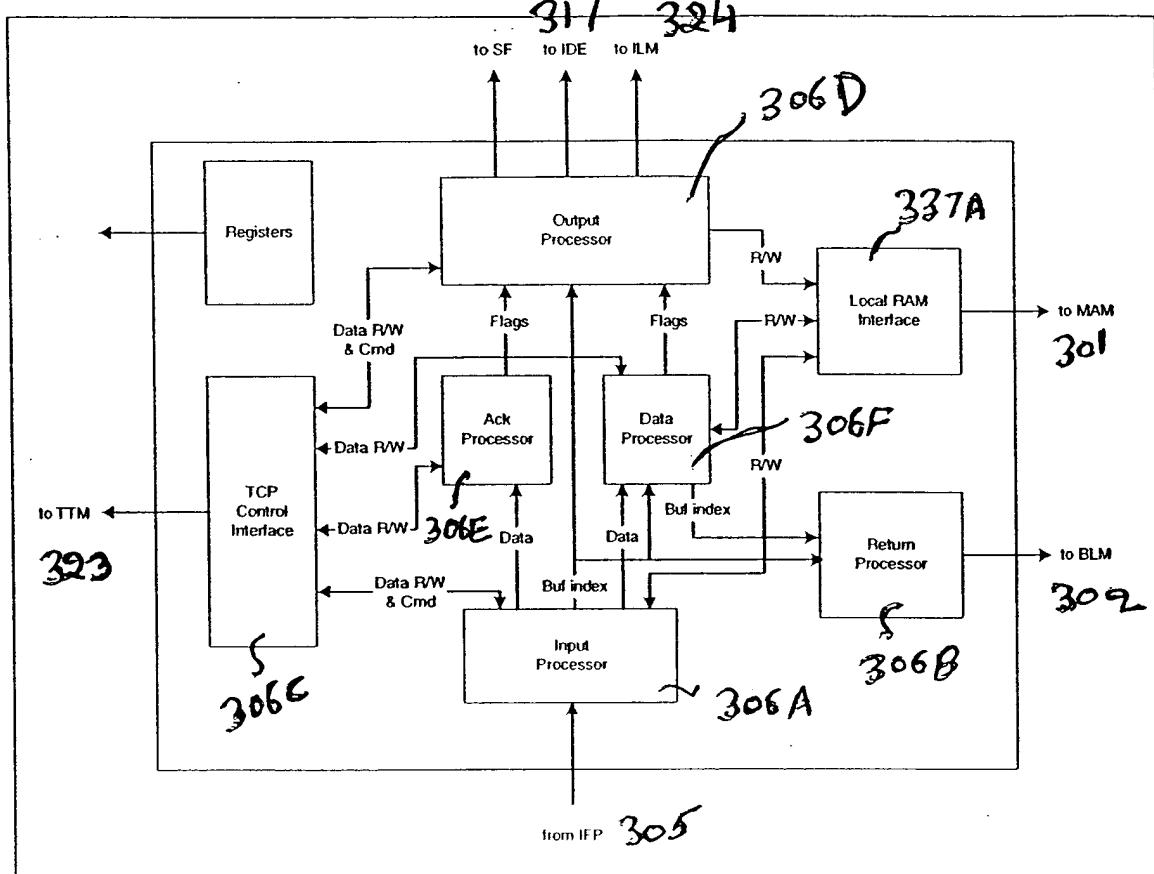


Figure 3B -

306

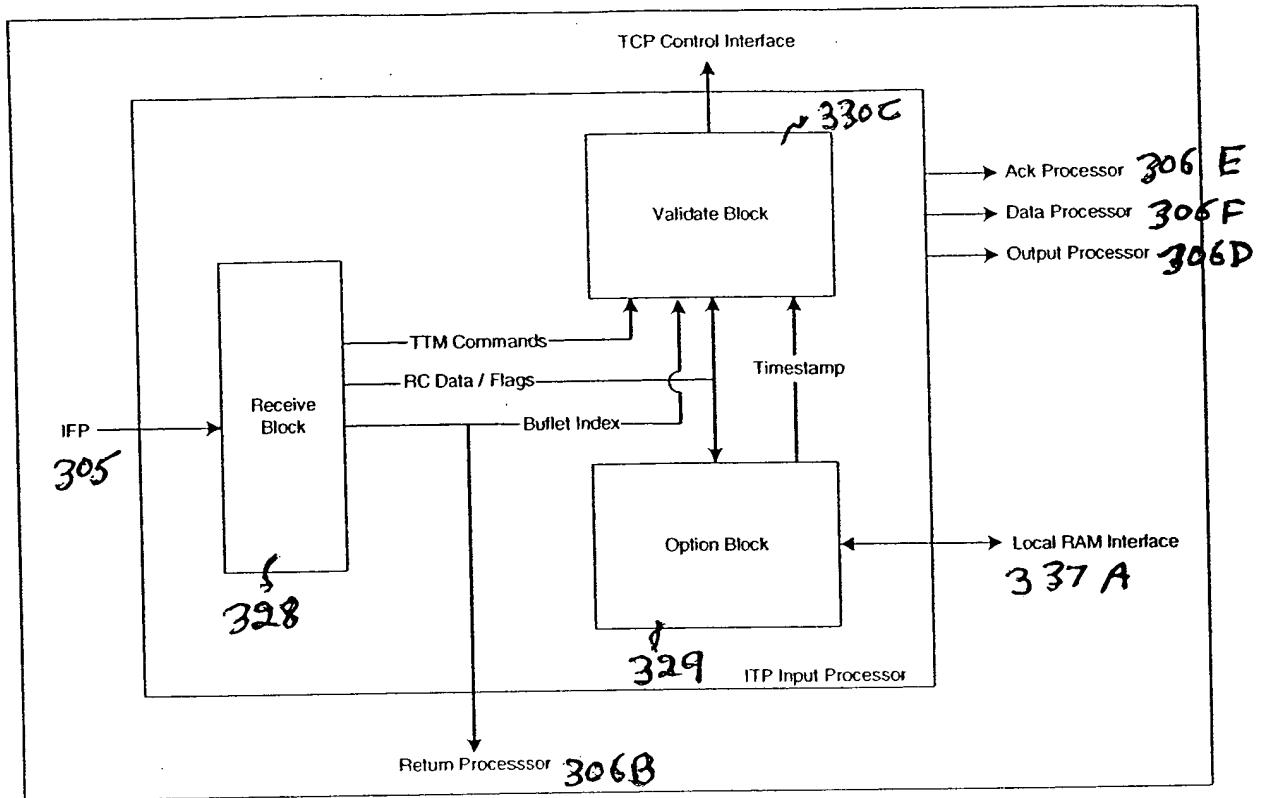


Figure 3C – ITP Input Processor

306 A

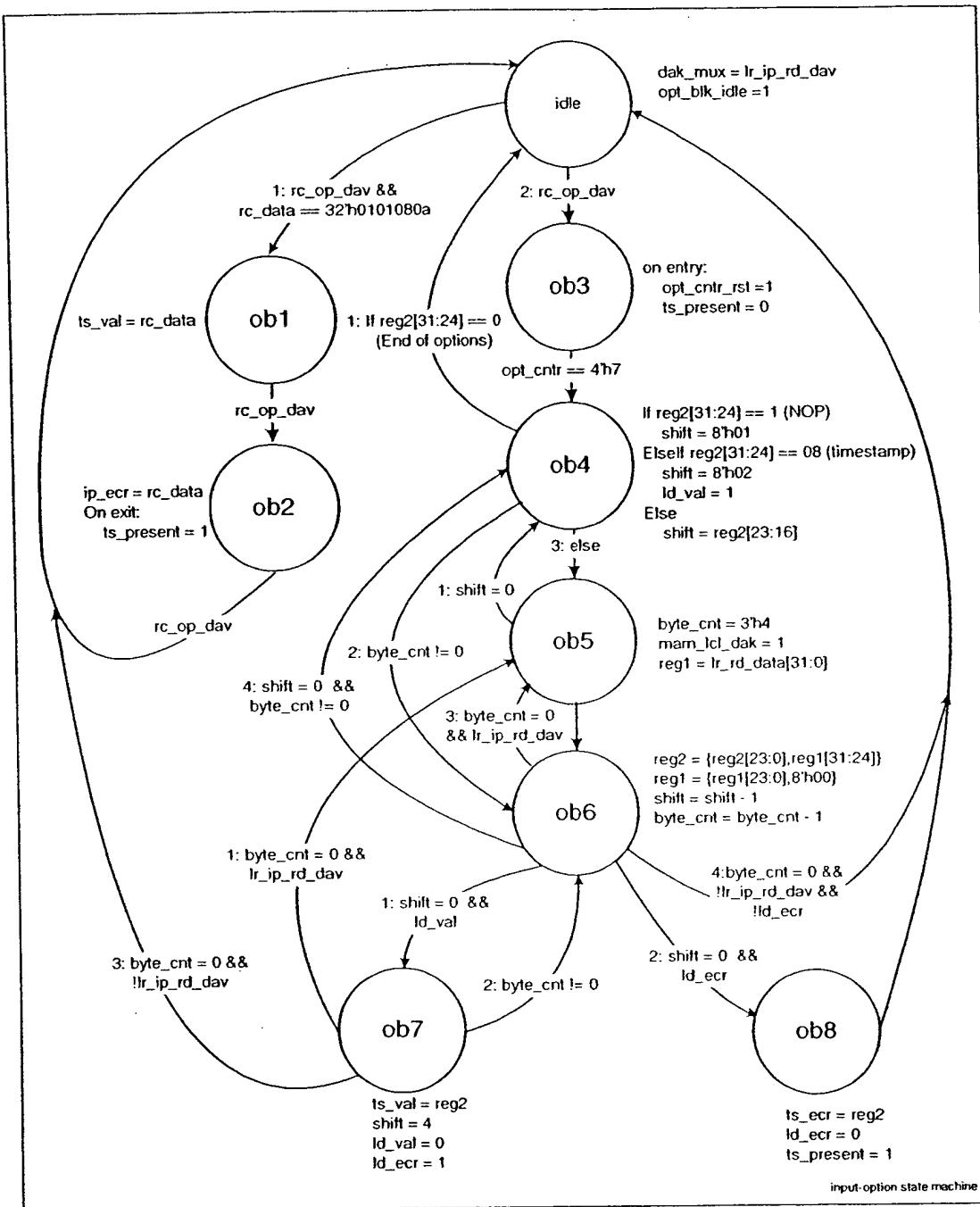


Figure 3C1 Input Processor Option Block State Machine

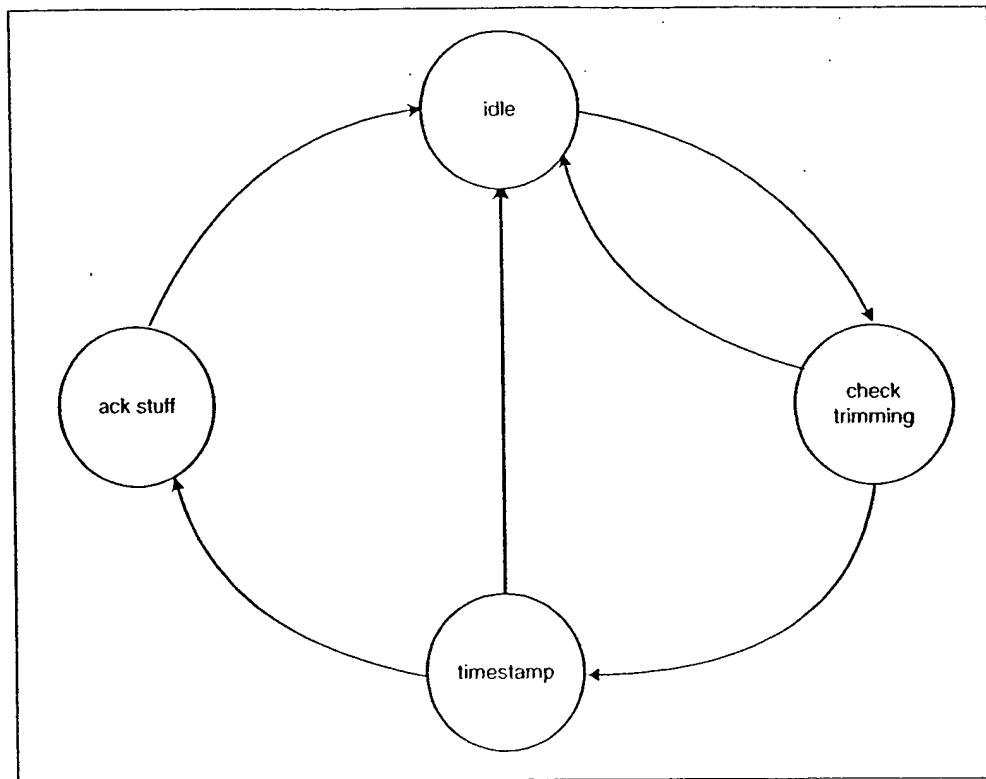


Figure 3C2: Validation State Machine

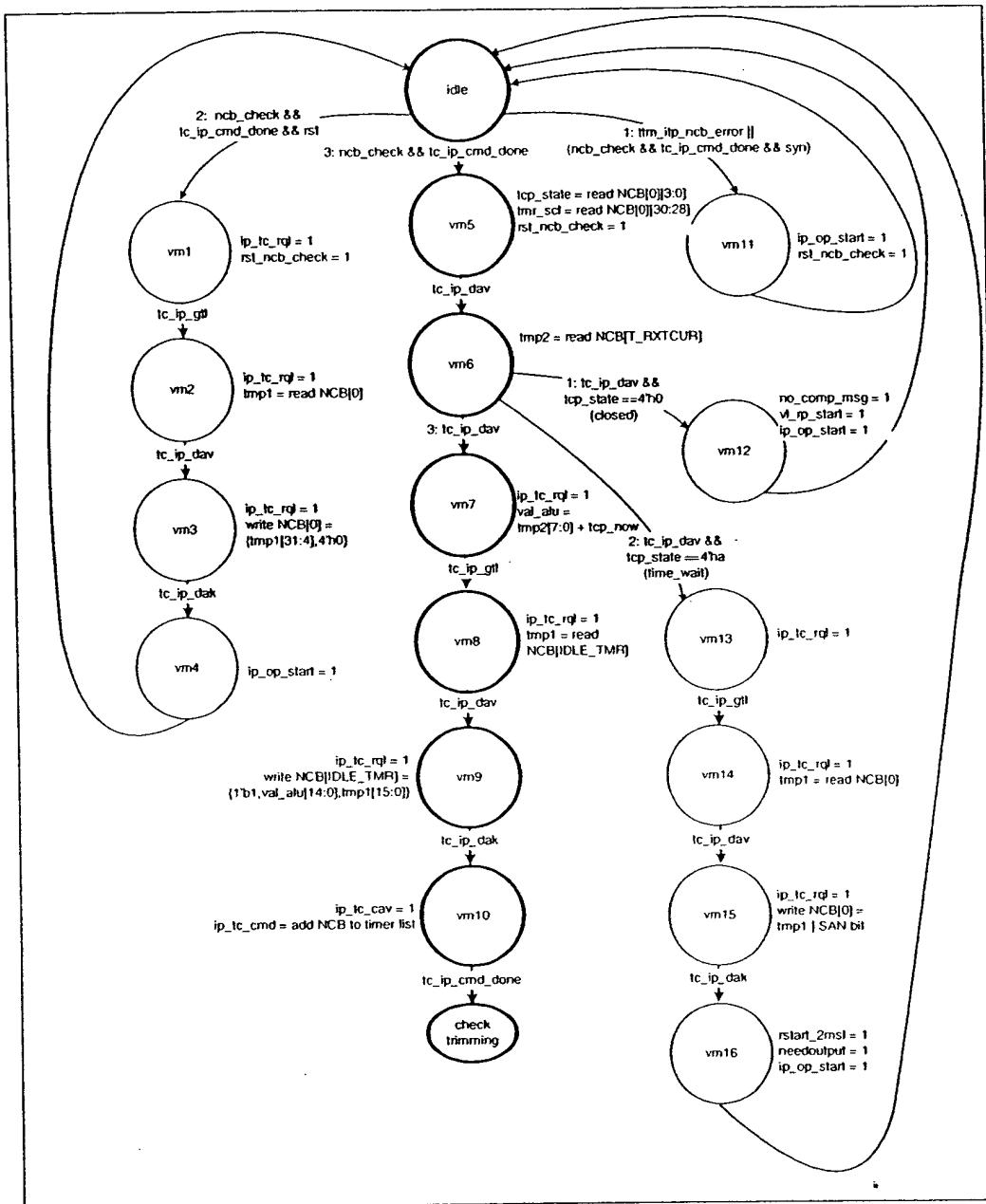


Figure 3C3: Validation State Machine for Reset, SYN or Invalid State

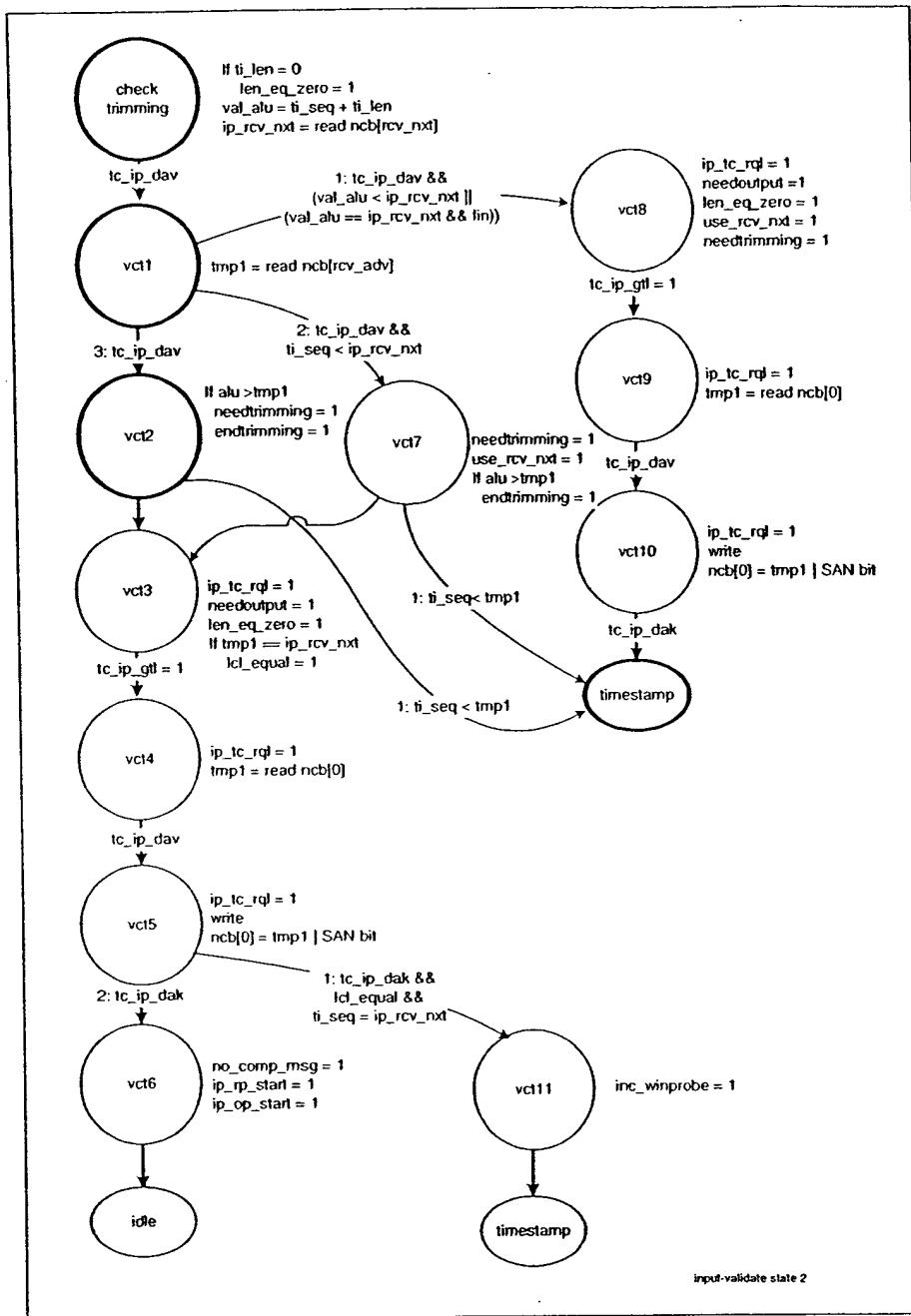


Figure 3C4: Validation State Machine for Trimming

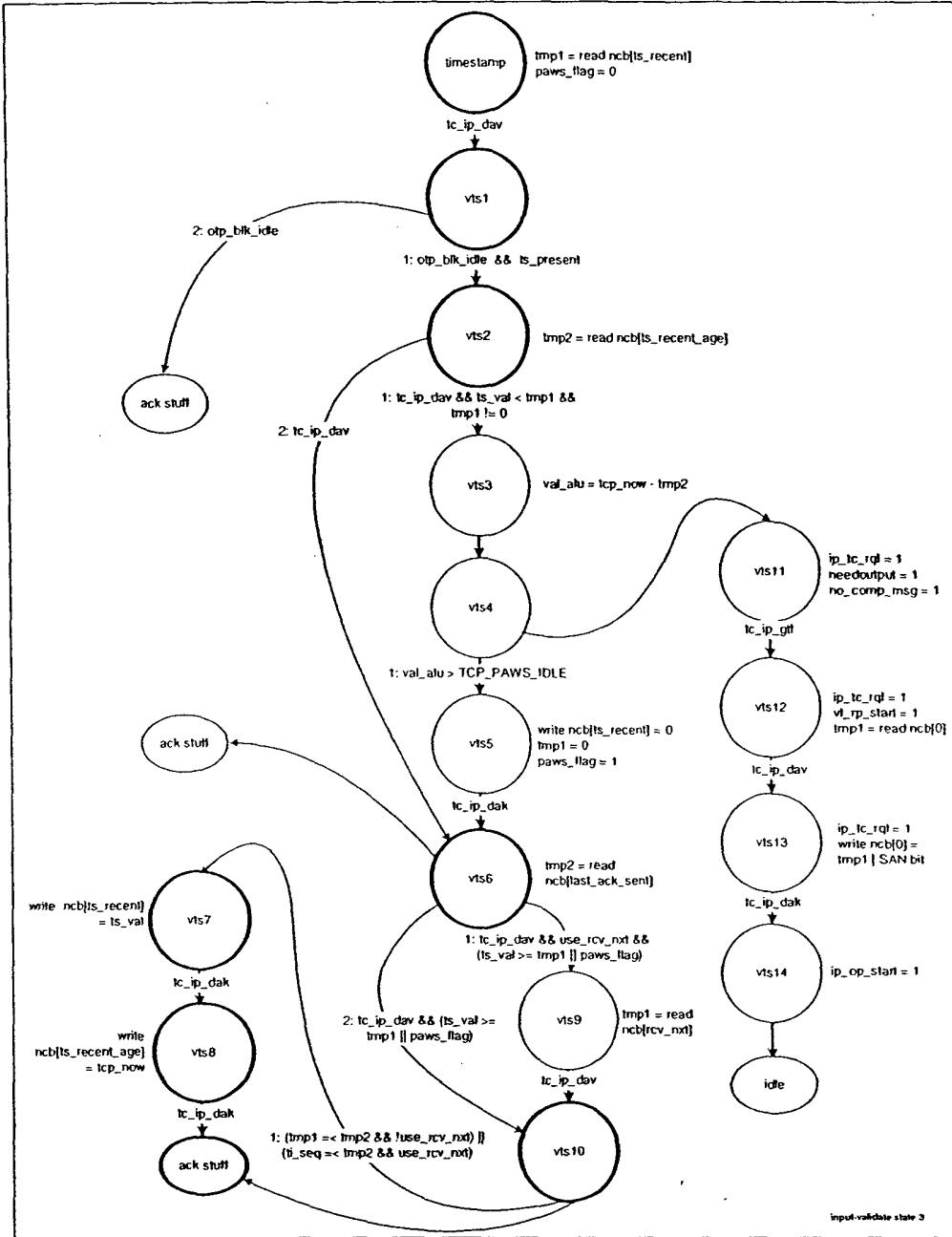


Figure 3C5: Validation State machine for Timestamp functionality

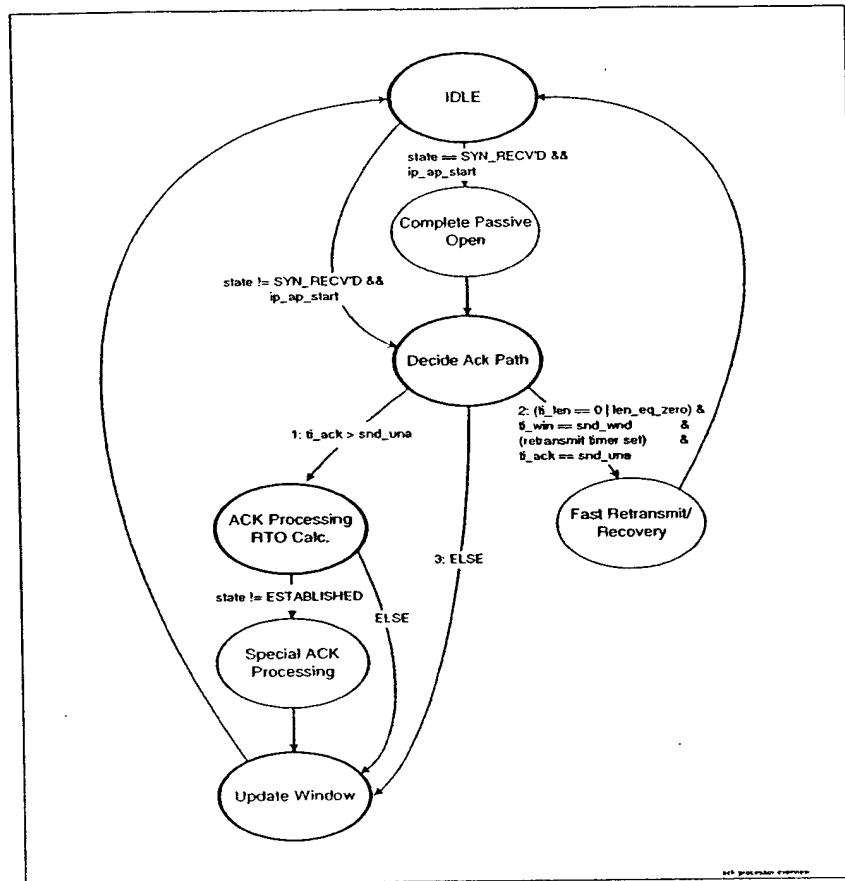


Figure3C6– ACK Processor

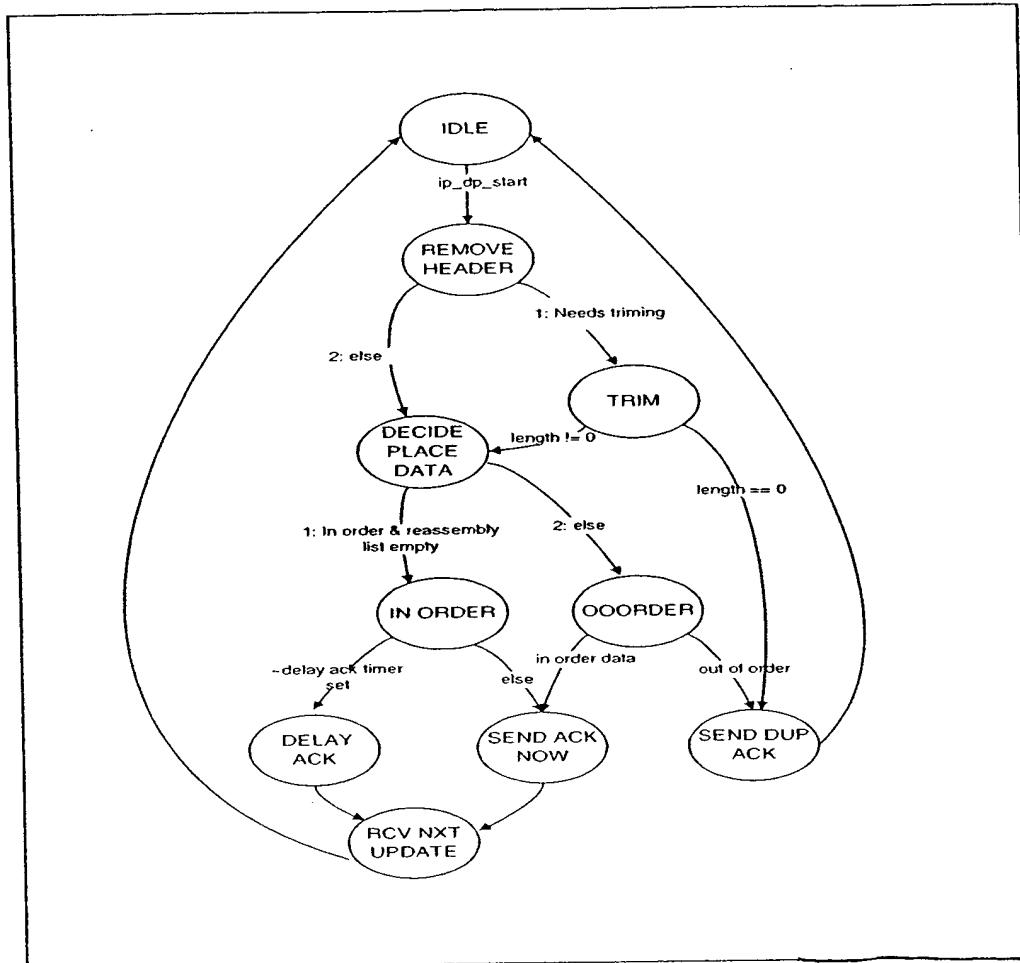


Figure 3C7 Data Processing State Machine

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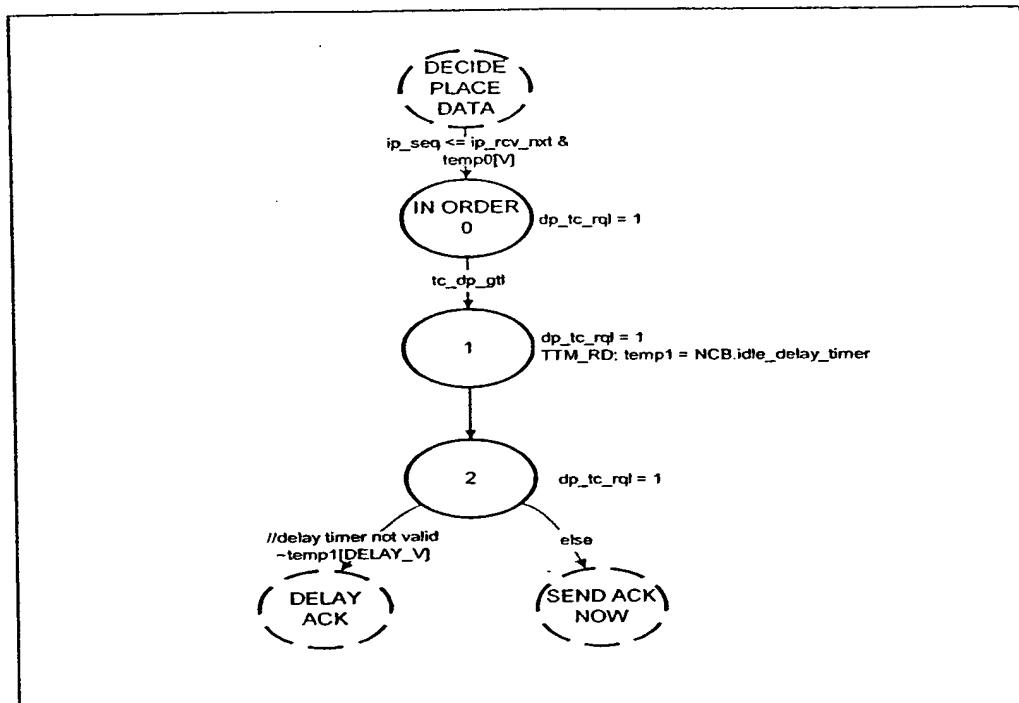


Figure 3C8 – Data Process Inorder

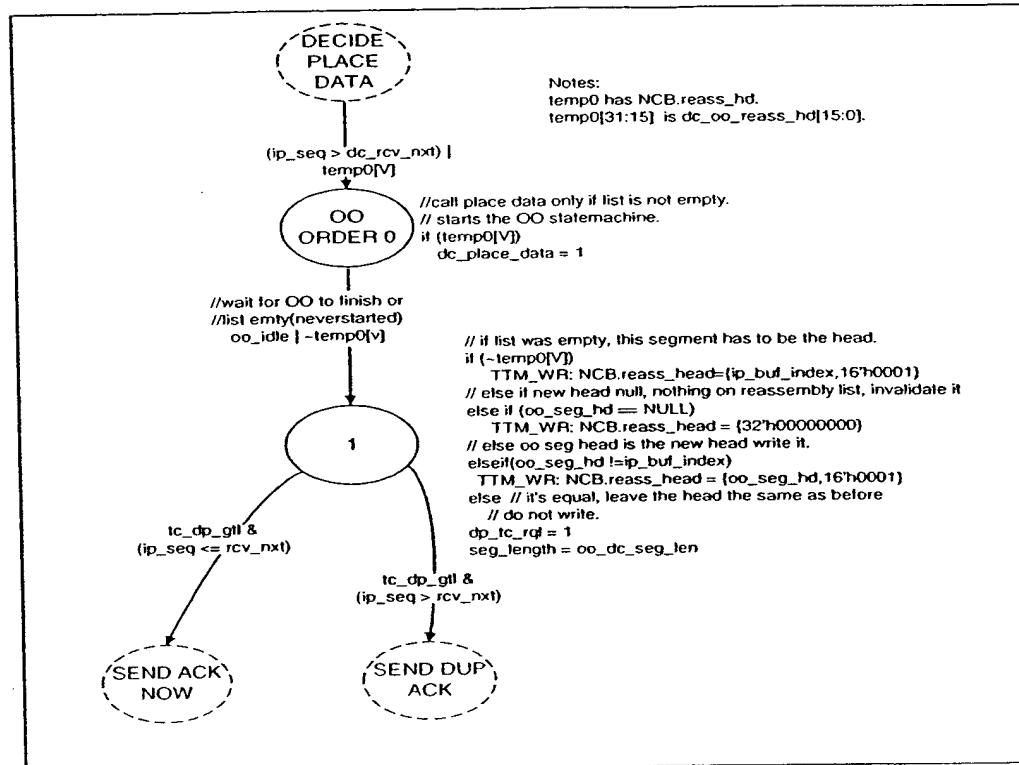


Figure 3C9 – Data Process Out of Order

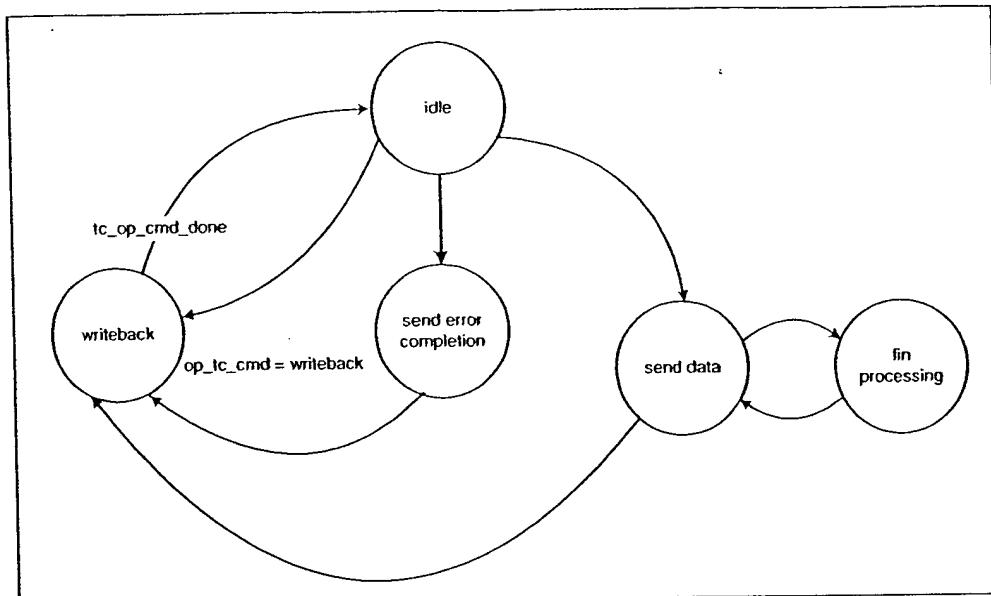


Figure 3C10 - ITP Output Processor

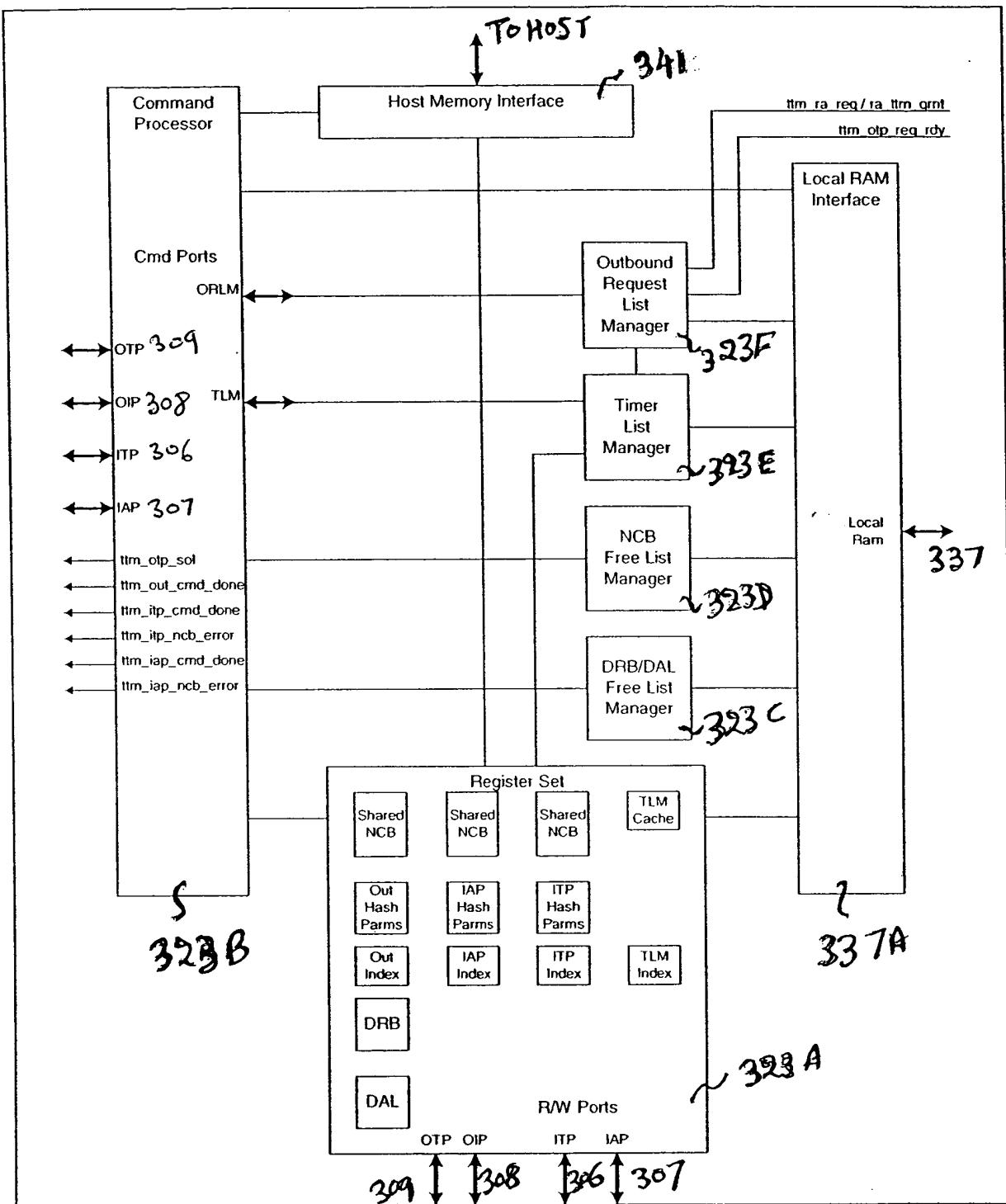


Figure 3D?

323

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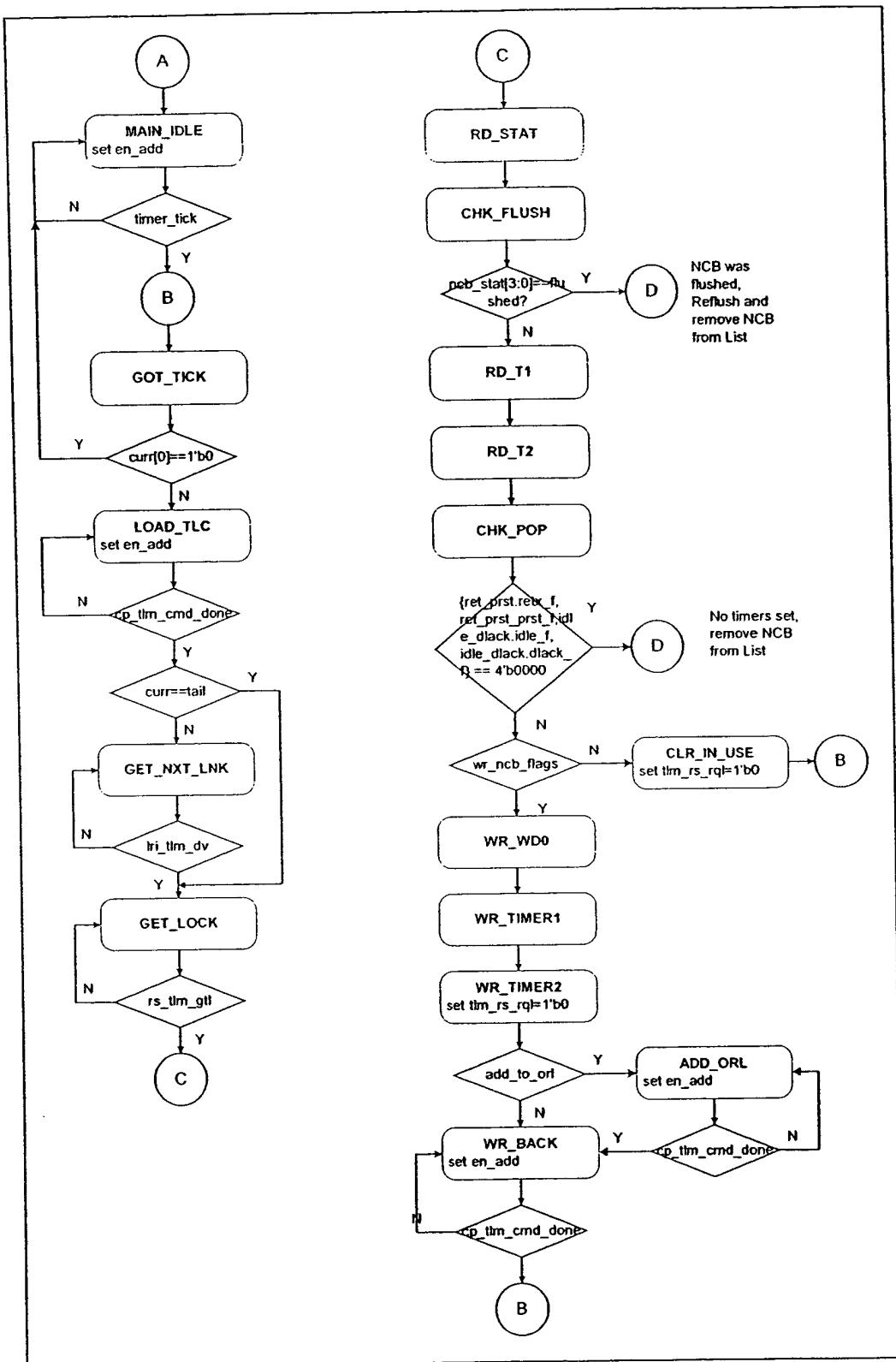


Figure 3D1 Timer List Manager State Machine

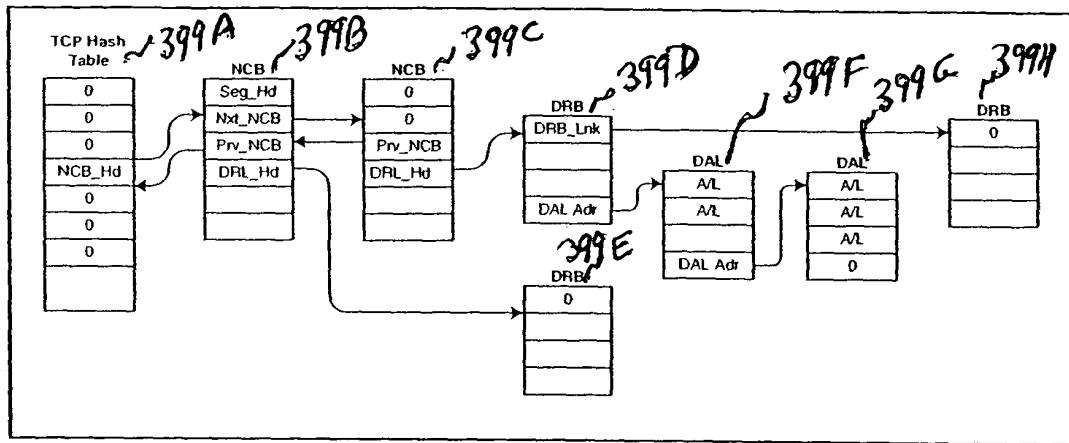


Figure 3E

399

402A

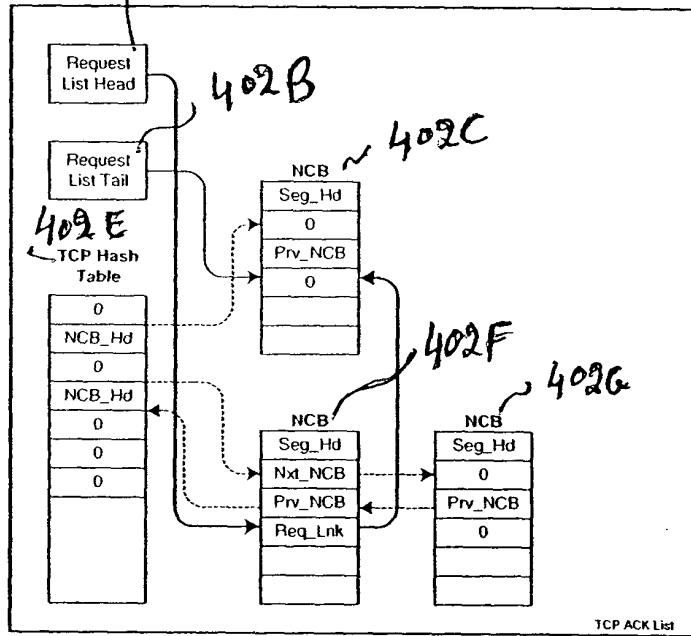


Figure 3F

402

402B

402C

402F

402G

260651

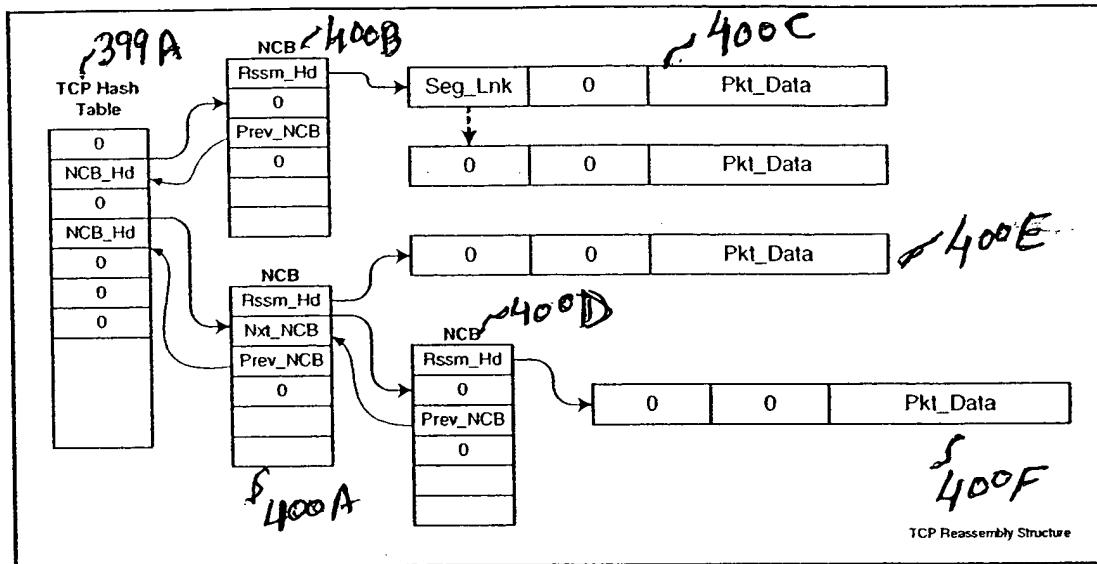


Figure 3G

40°

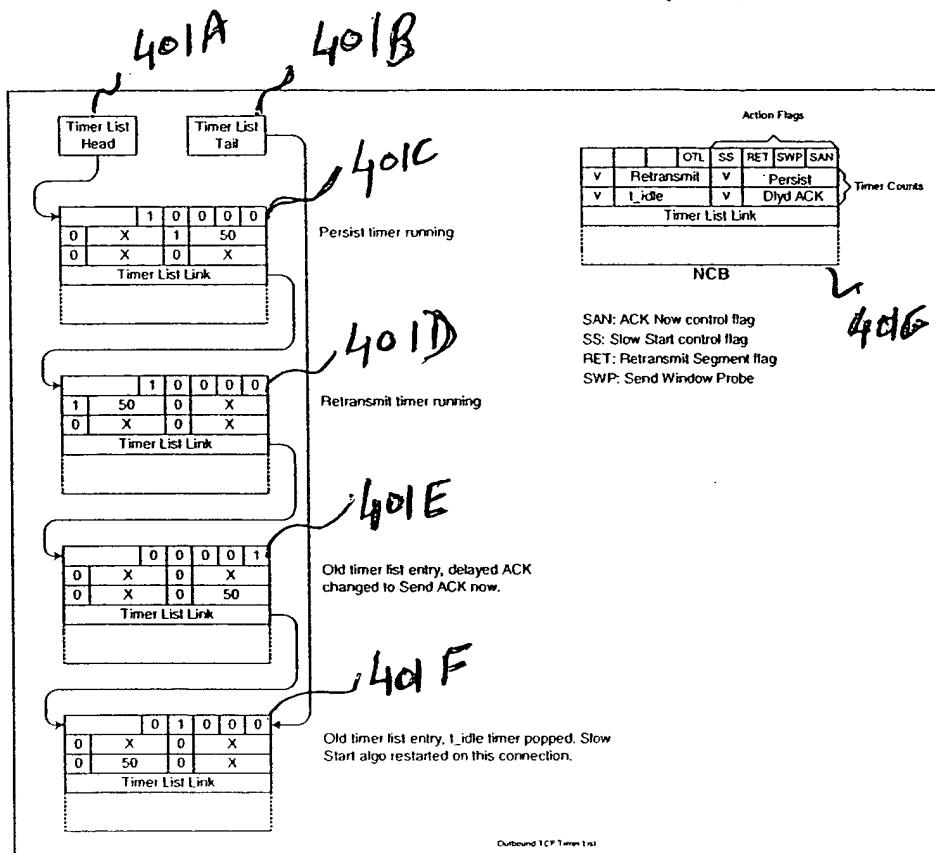


Figure 3H

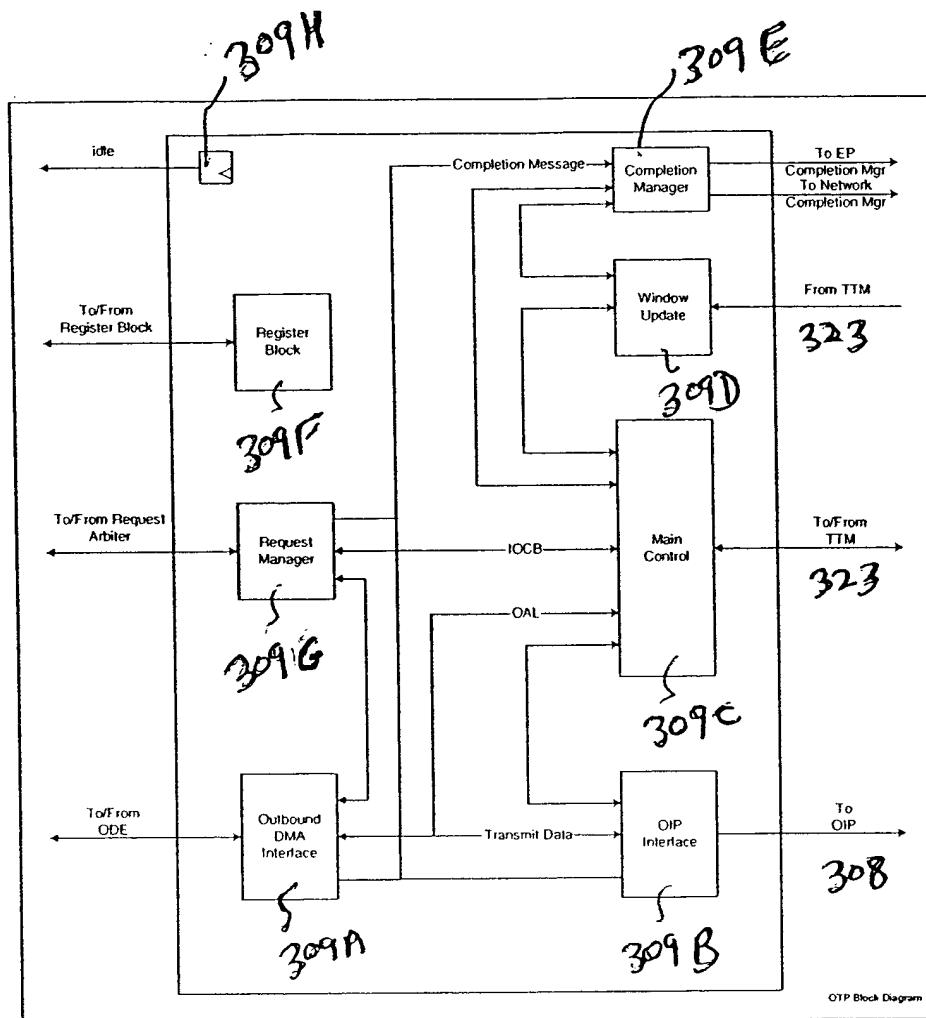


Figure 31

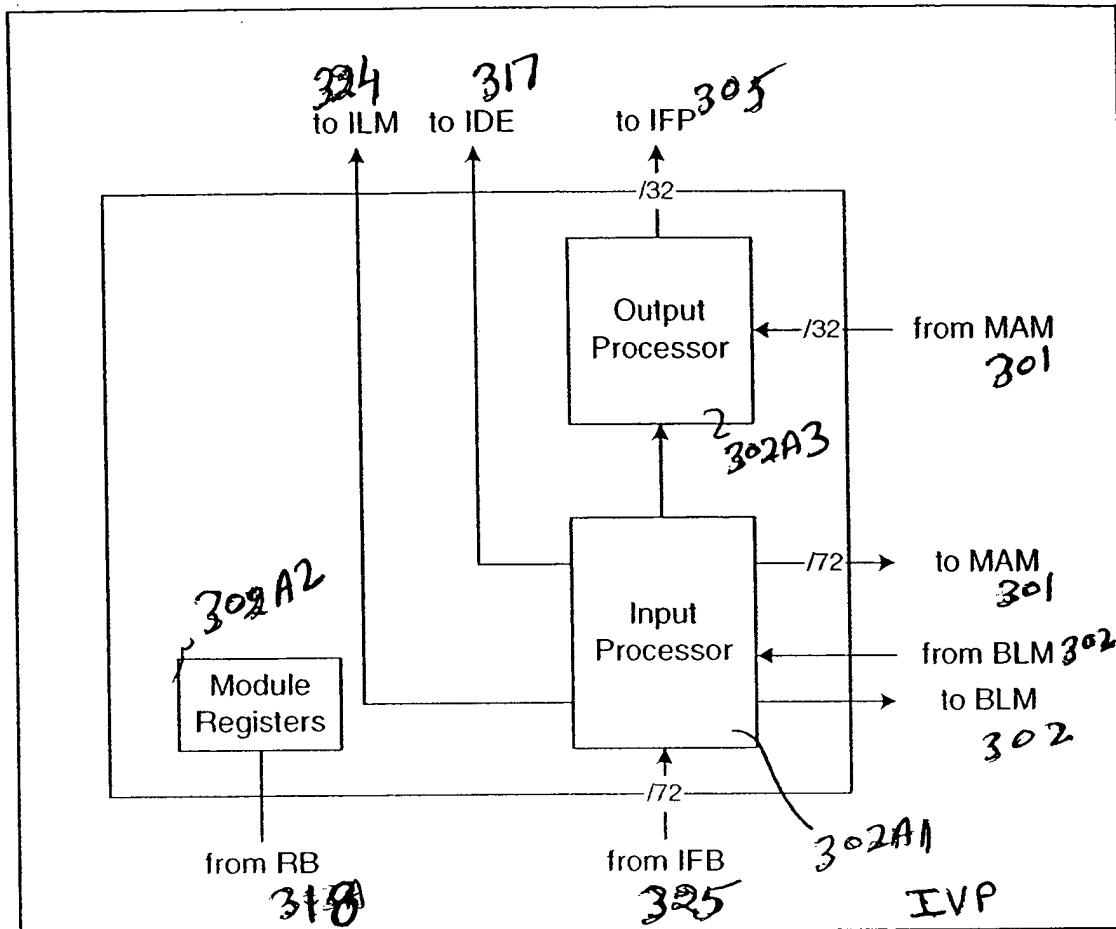


Figure 3J1

302 A

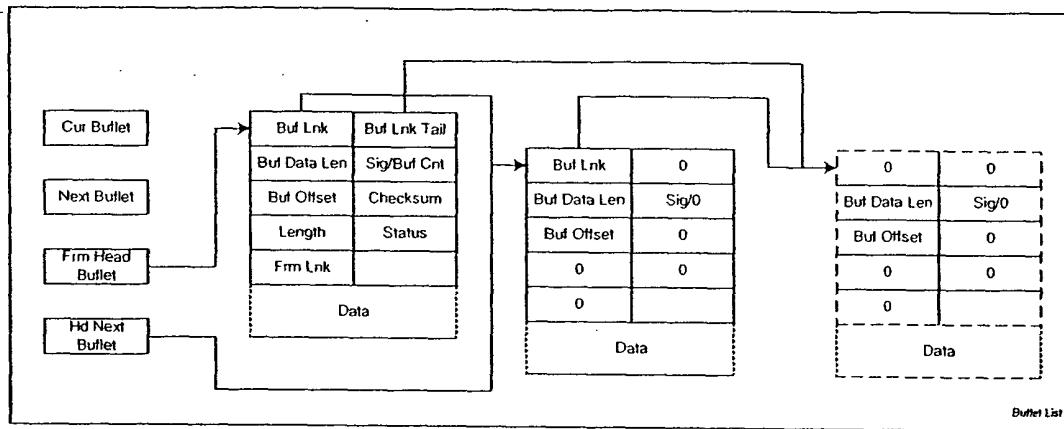


Figure 3J1 Buflet List Structure

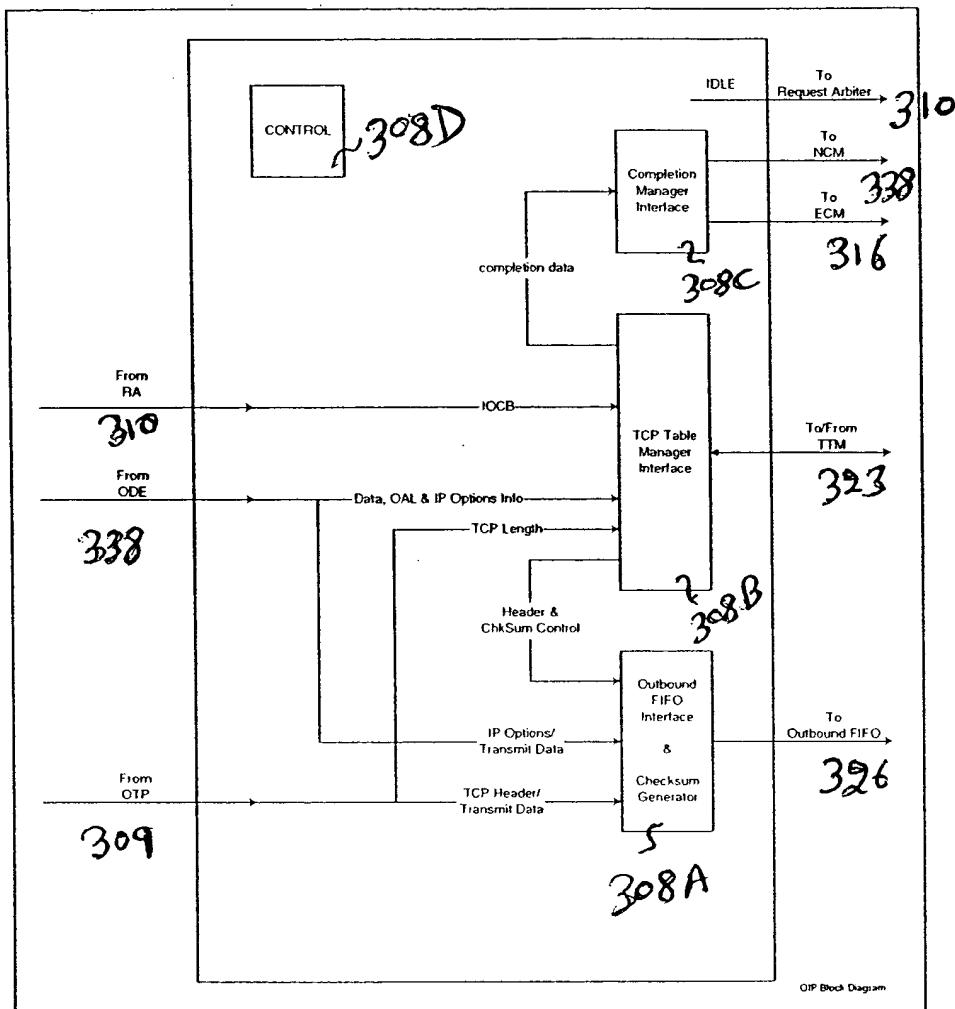


Figure 3K

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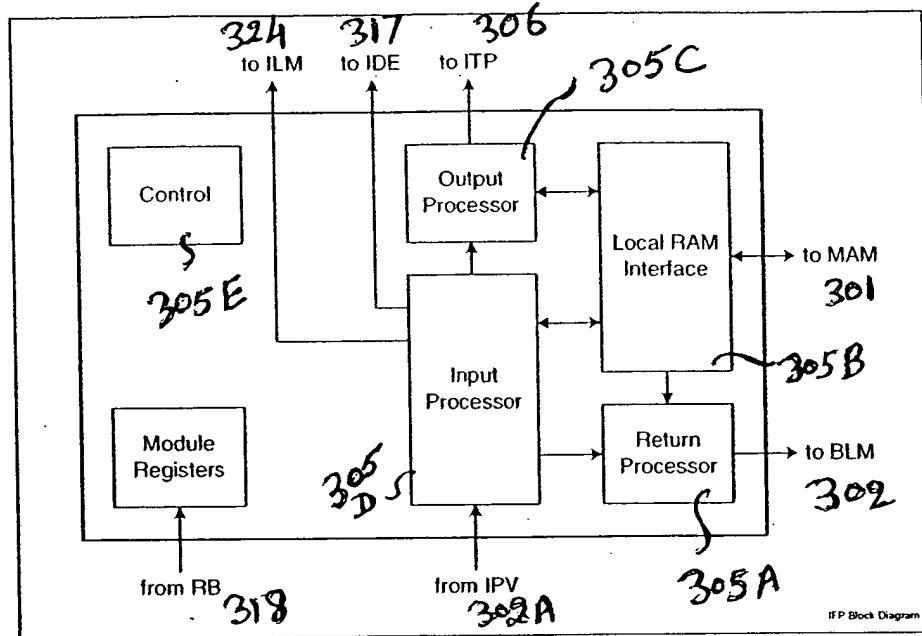


Figure 3L

305

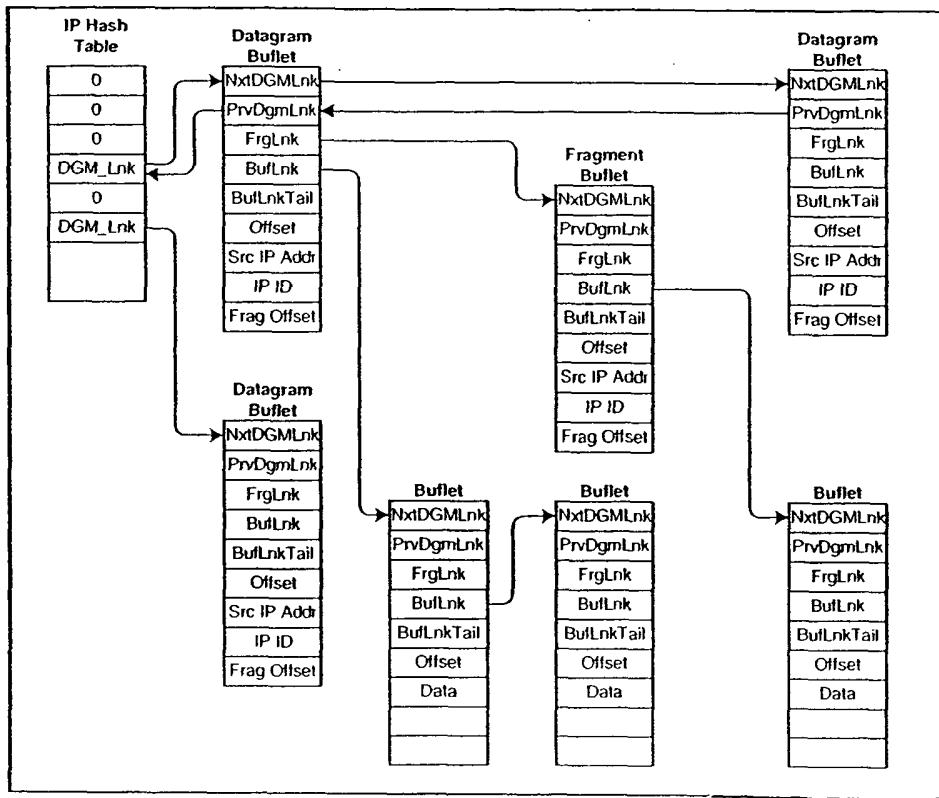


Figure 3L1:

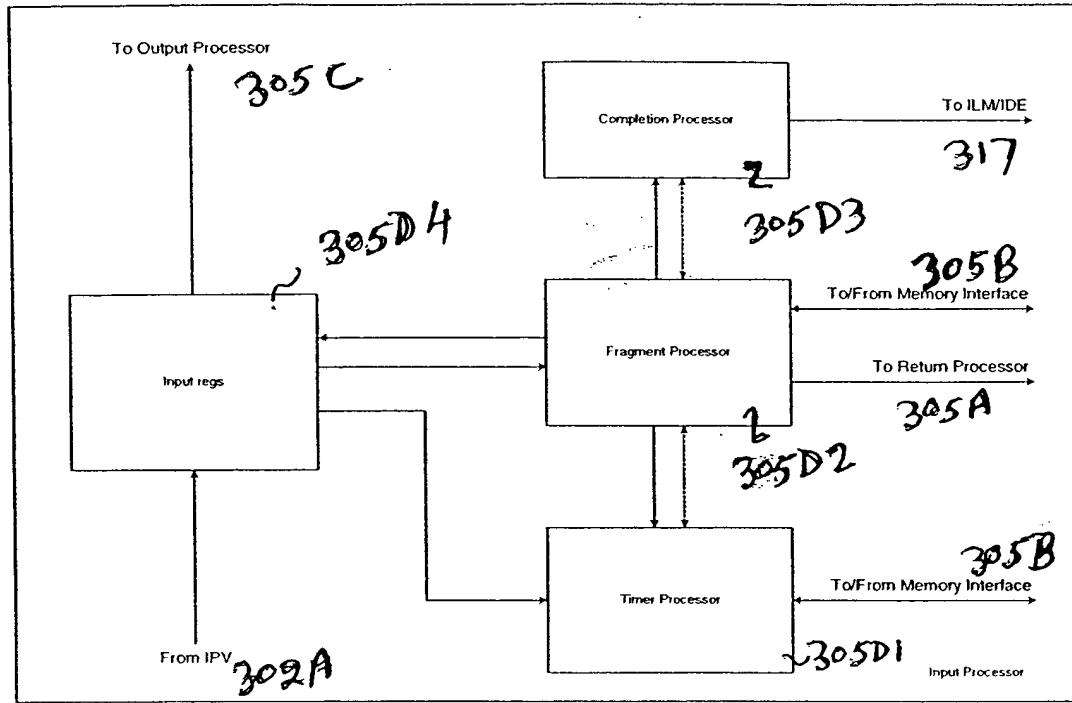


Figure 3L2

305D

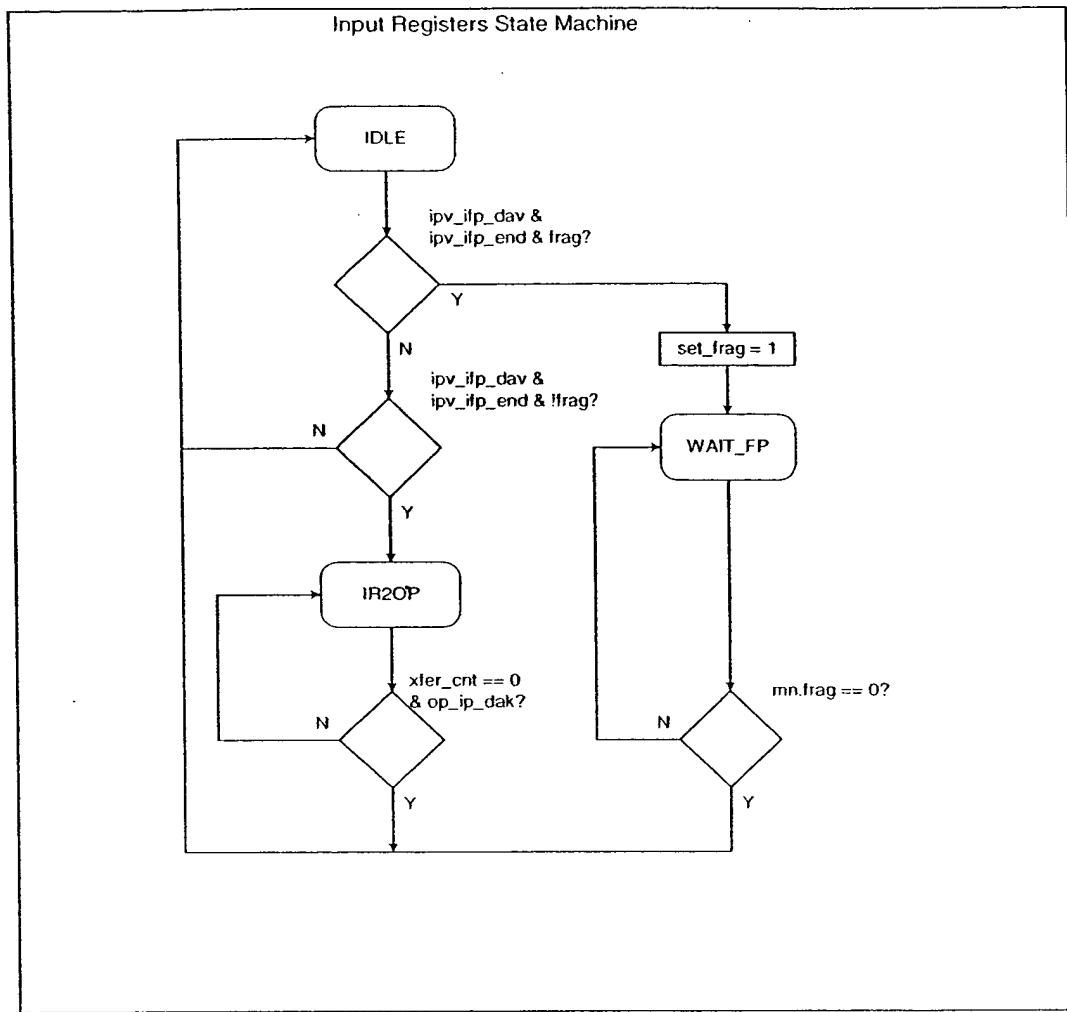


Figure 3L3

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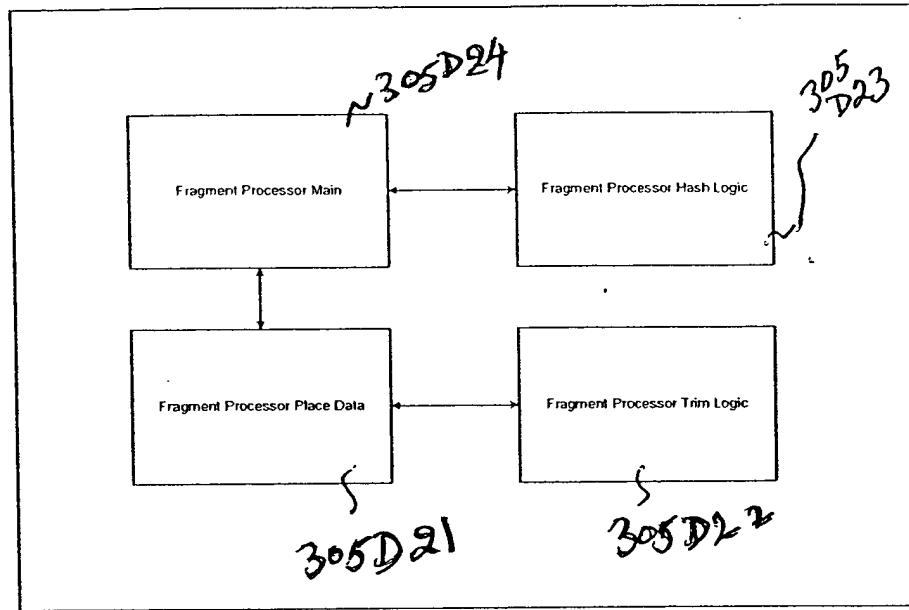


Figure 3L4

305D2

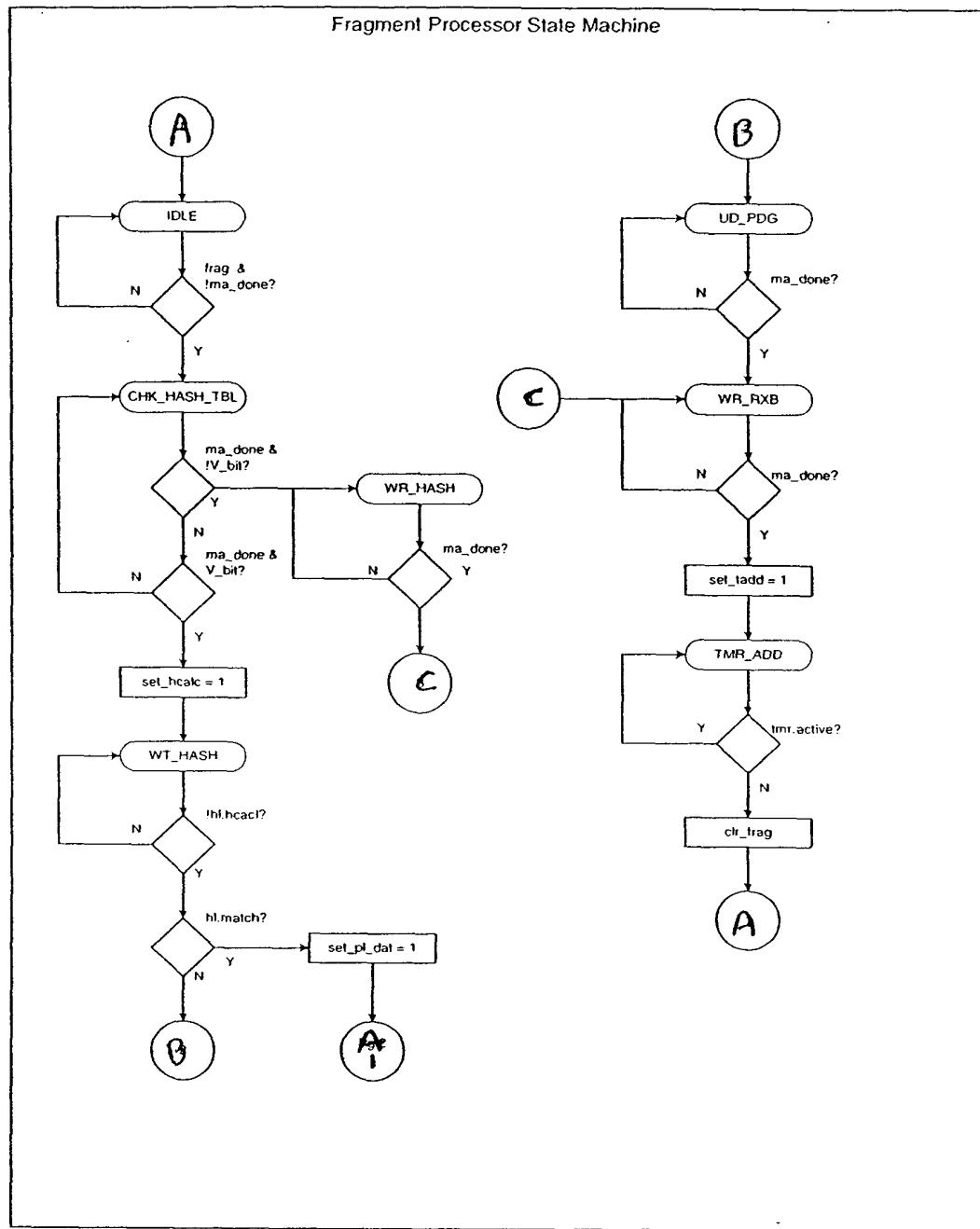


Figure 3L5A I

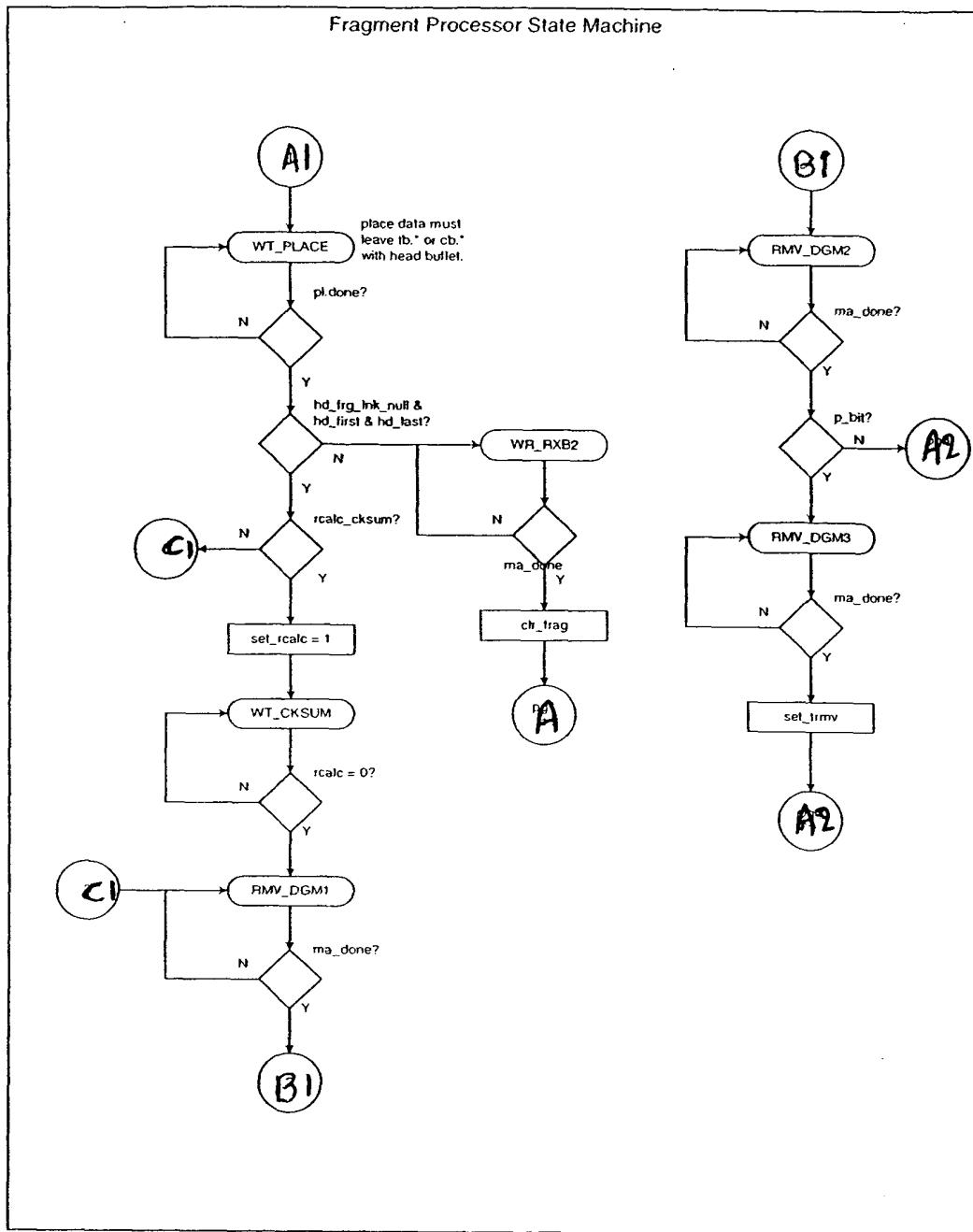


Figure 3L5B_F

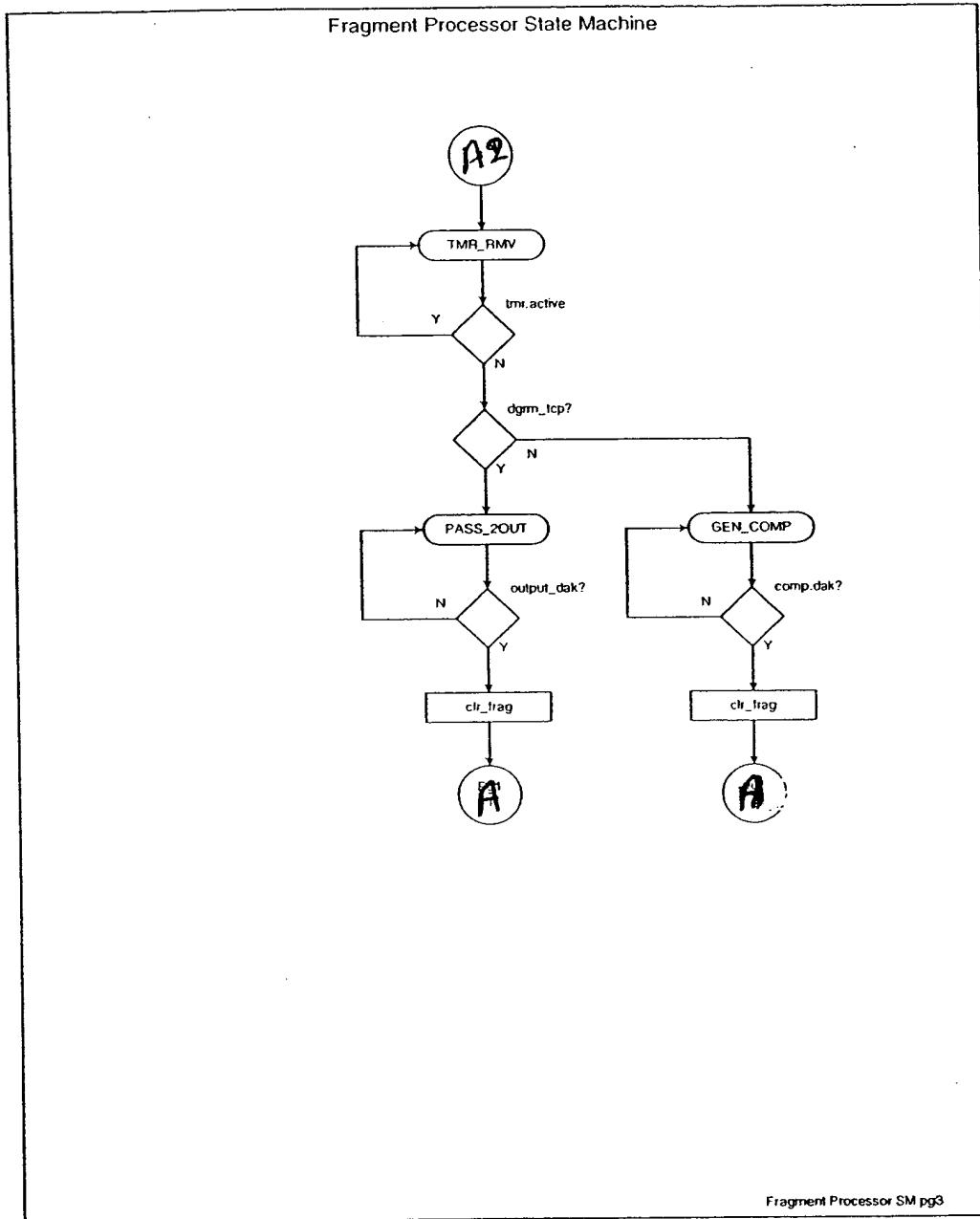


Figure 3L5C

40-651

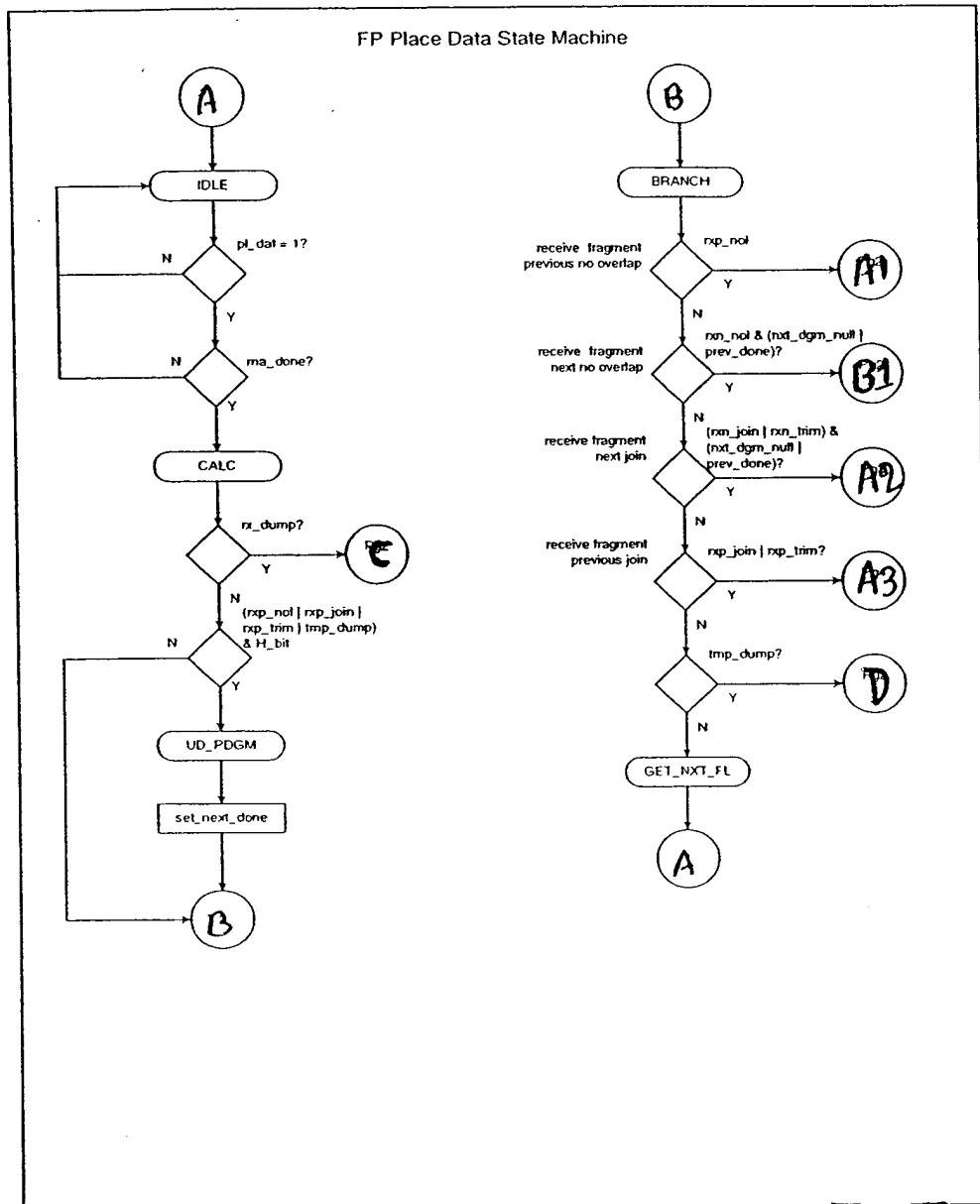


Figure 3L6A

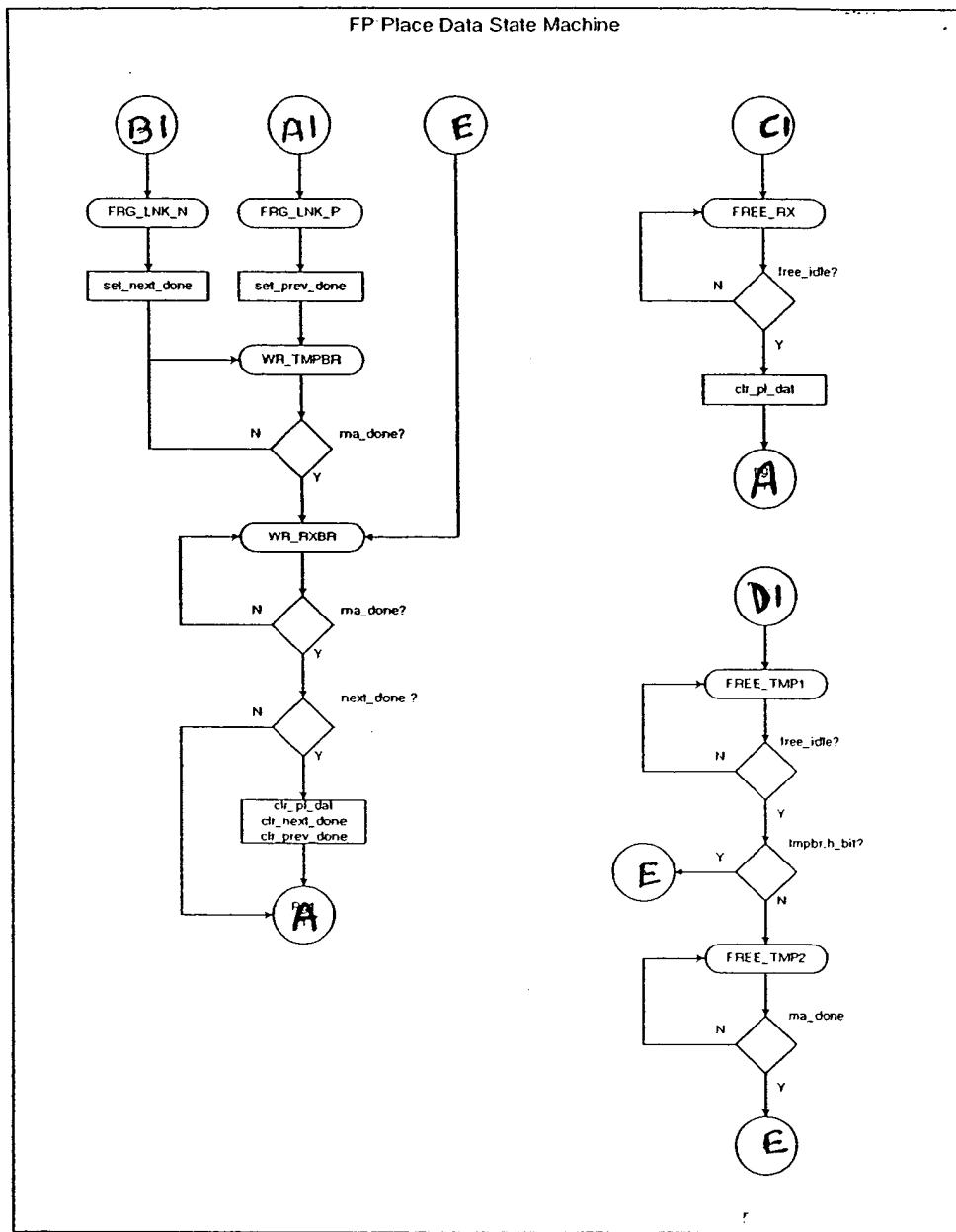


Figure 3L6 B

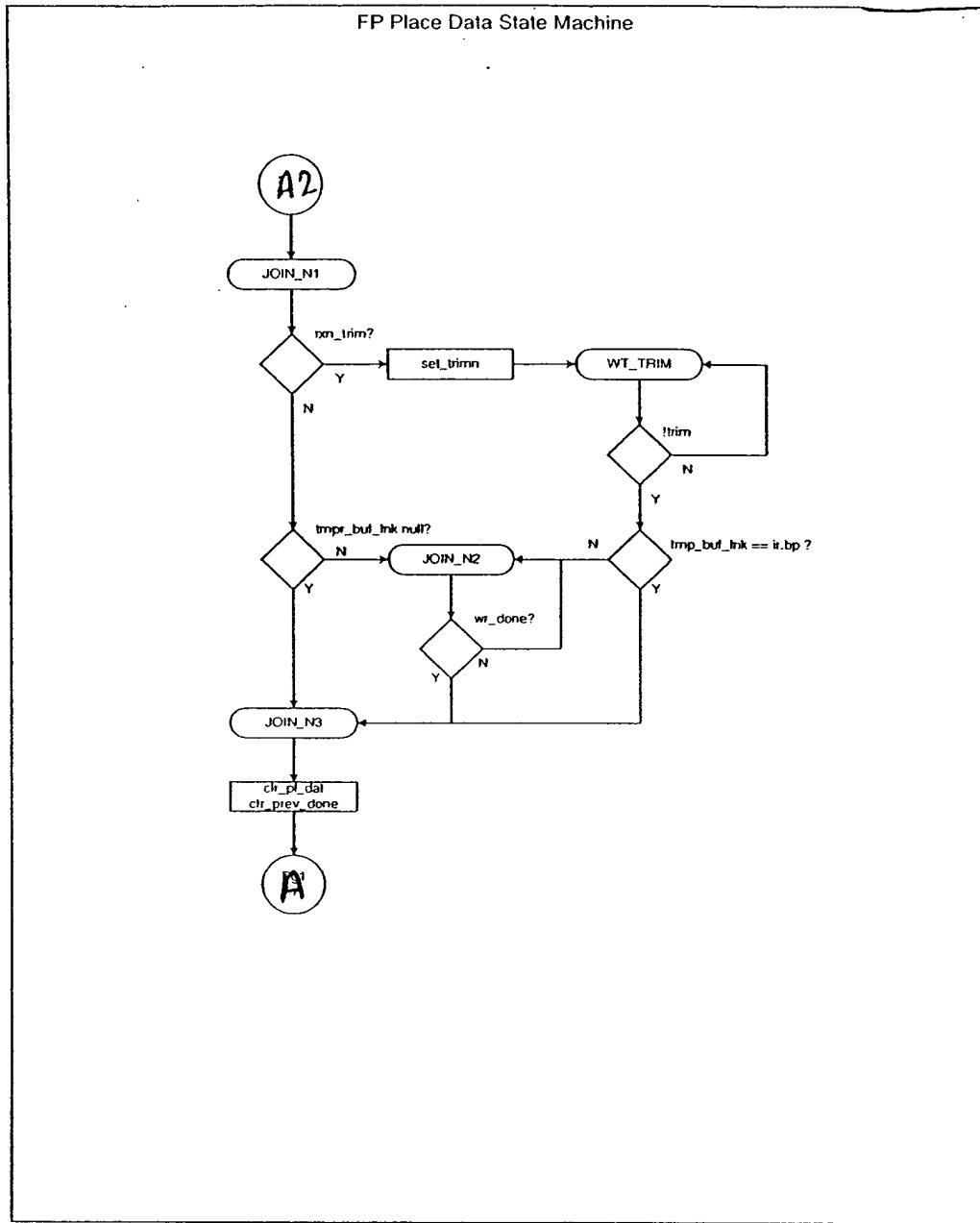


Figure 3L6C

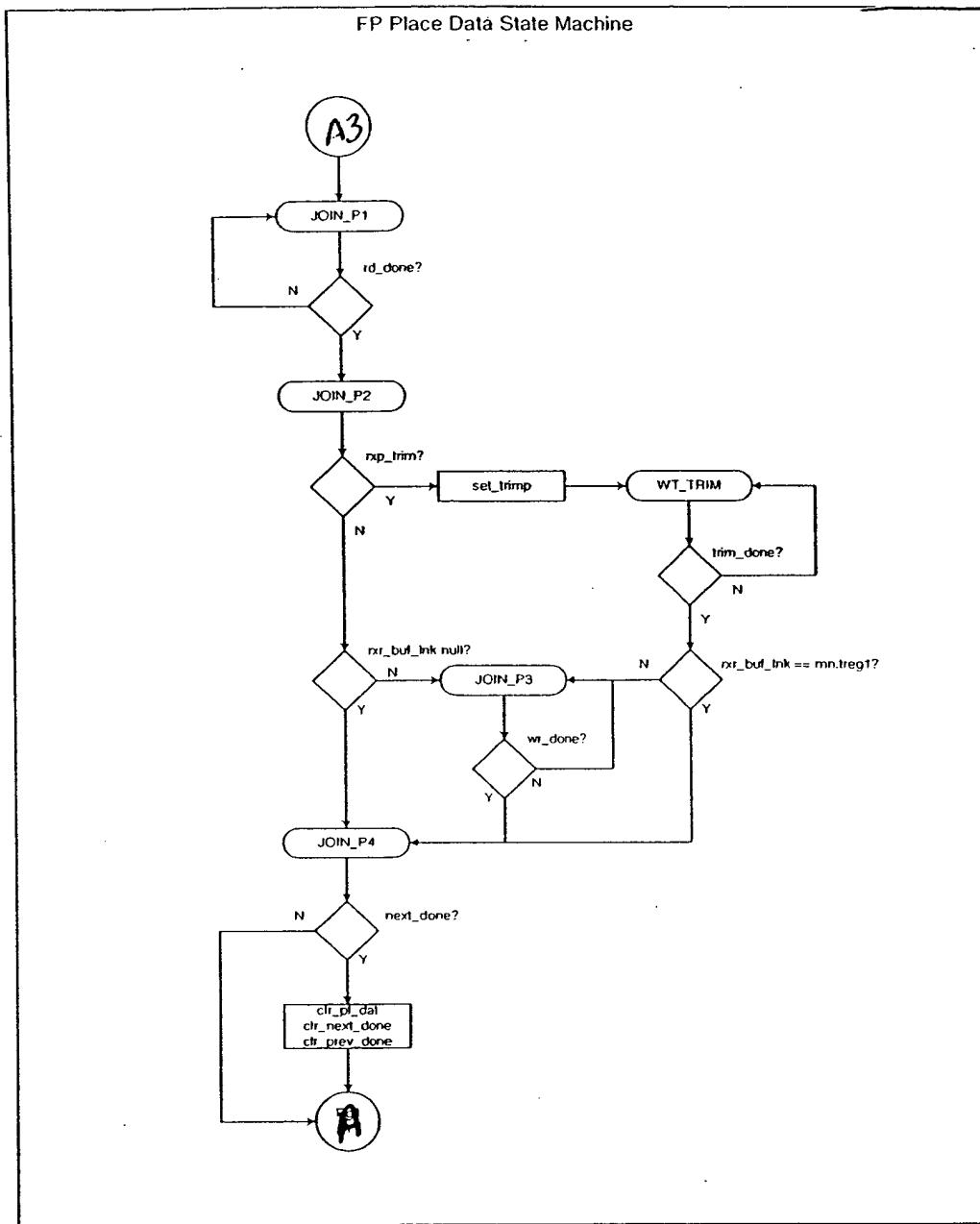


Figure 3L6D

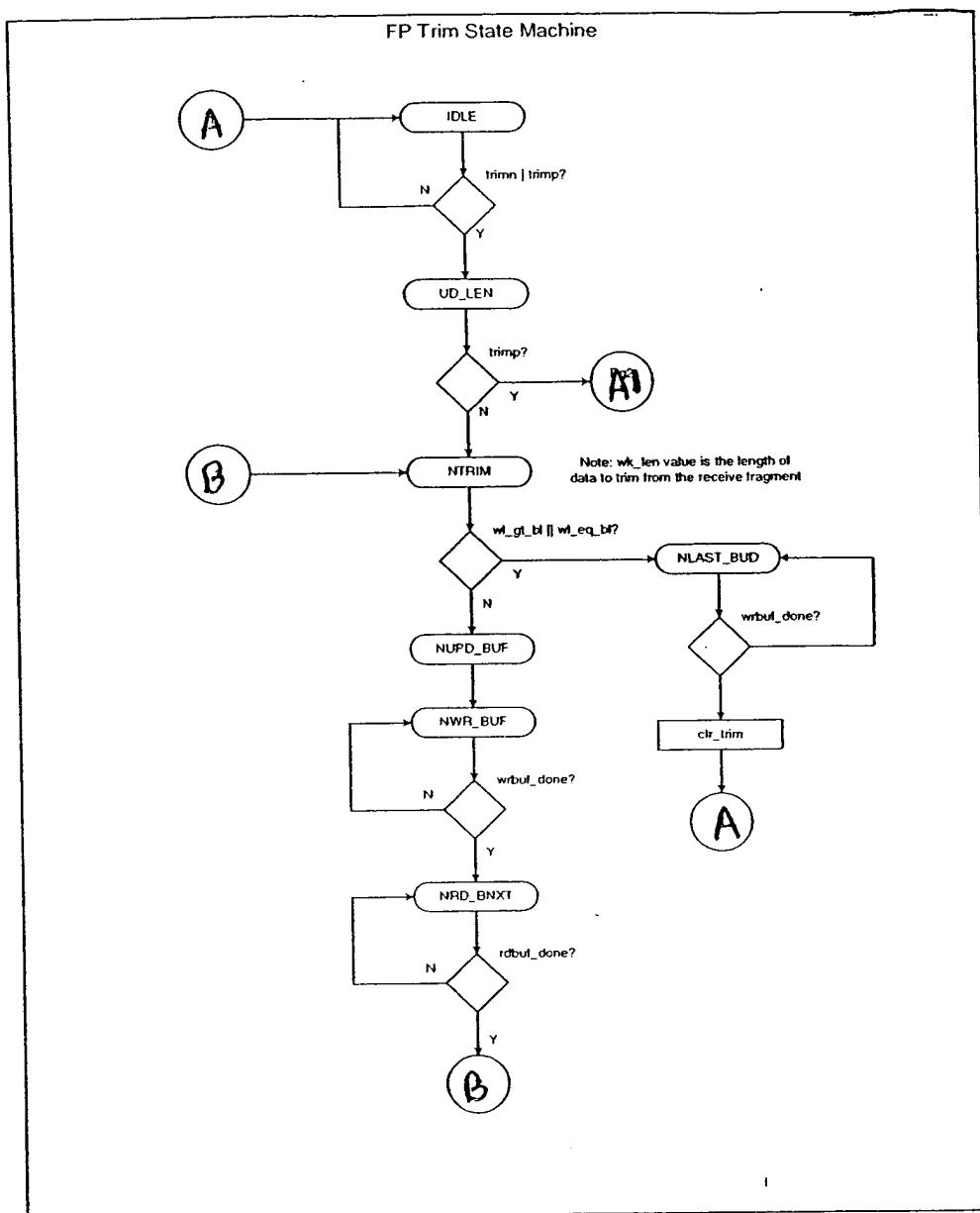


Figure 3L7A

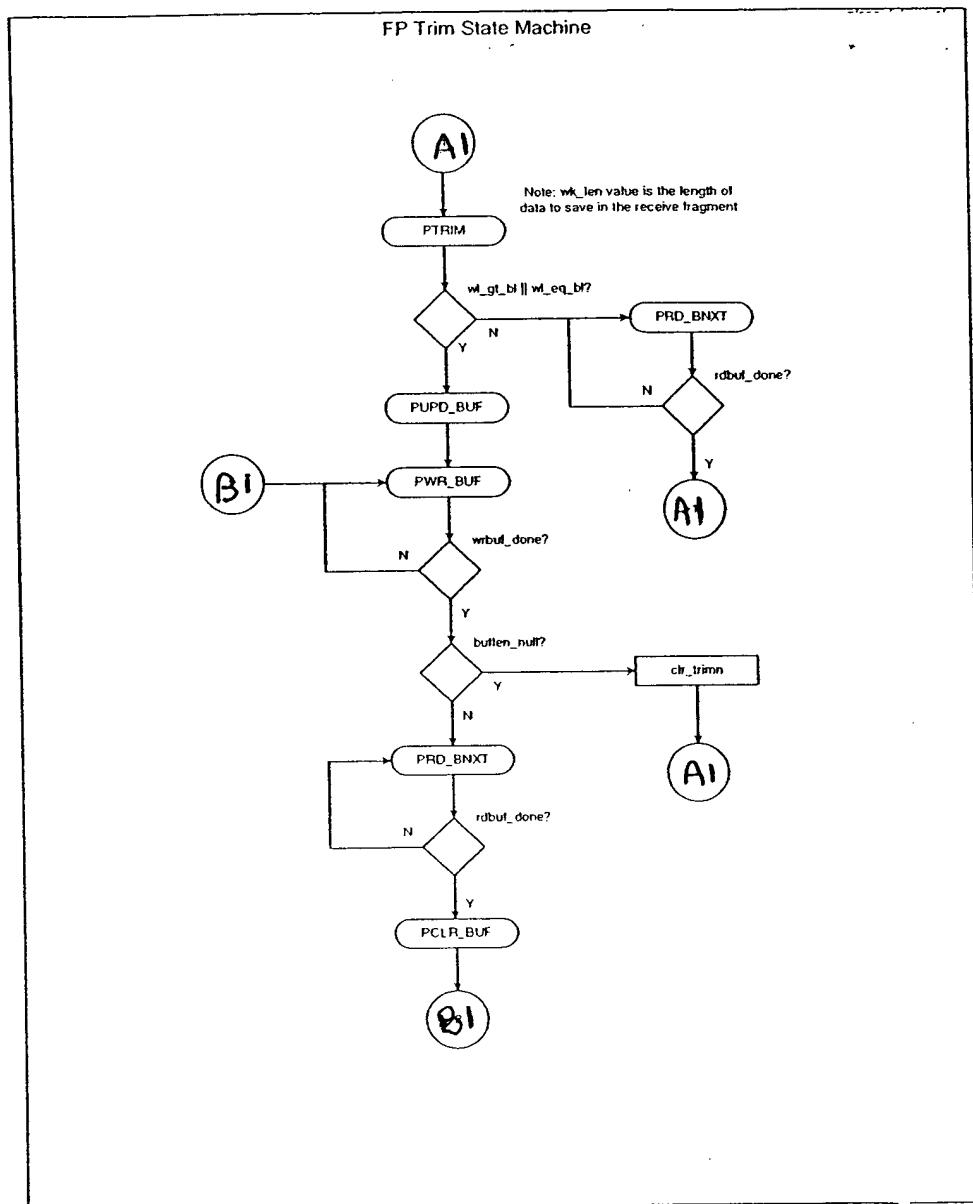


Figure 3L7B:

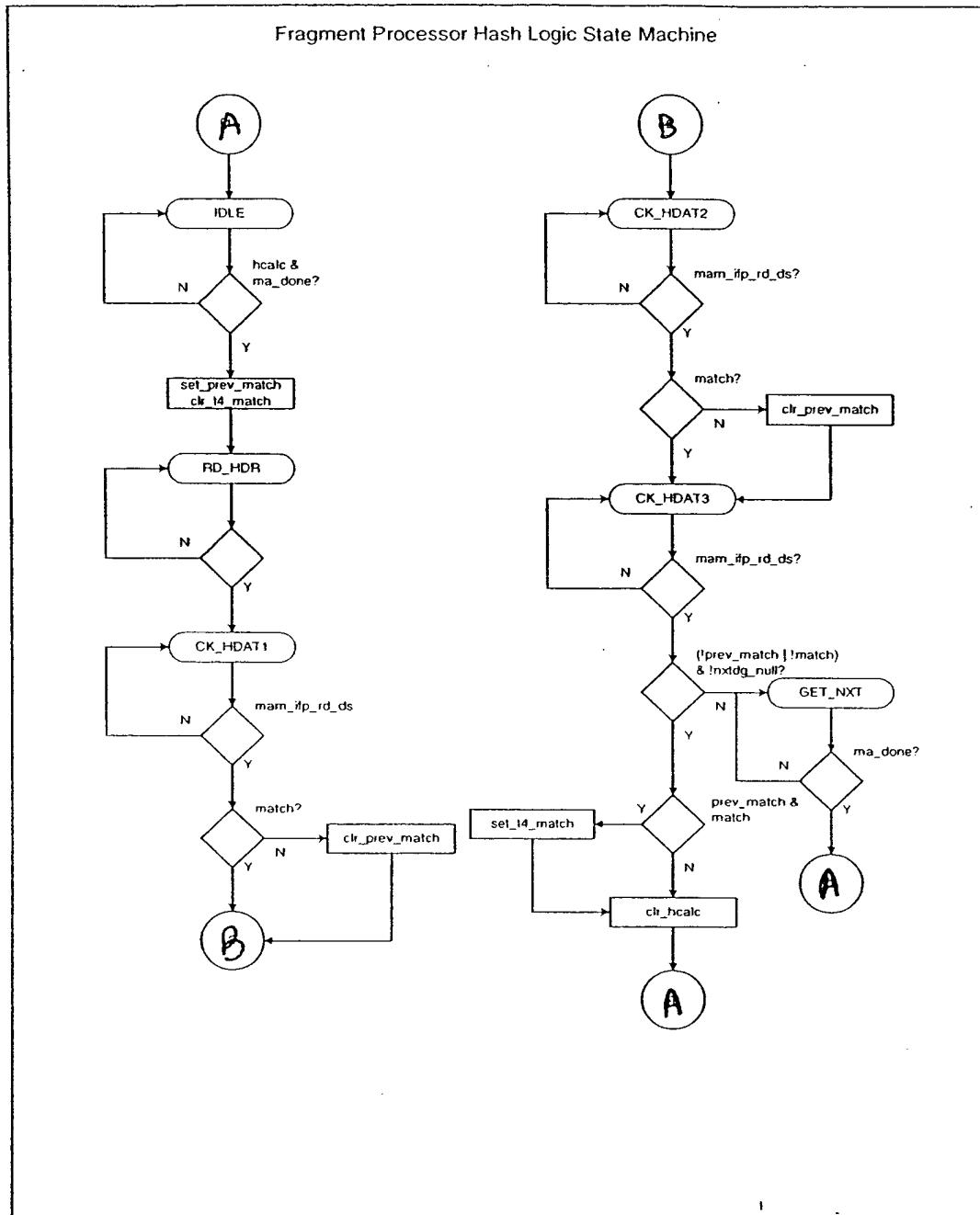


Figure 3L8]

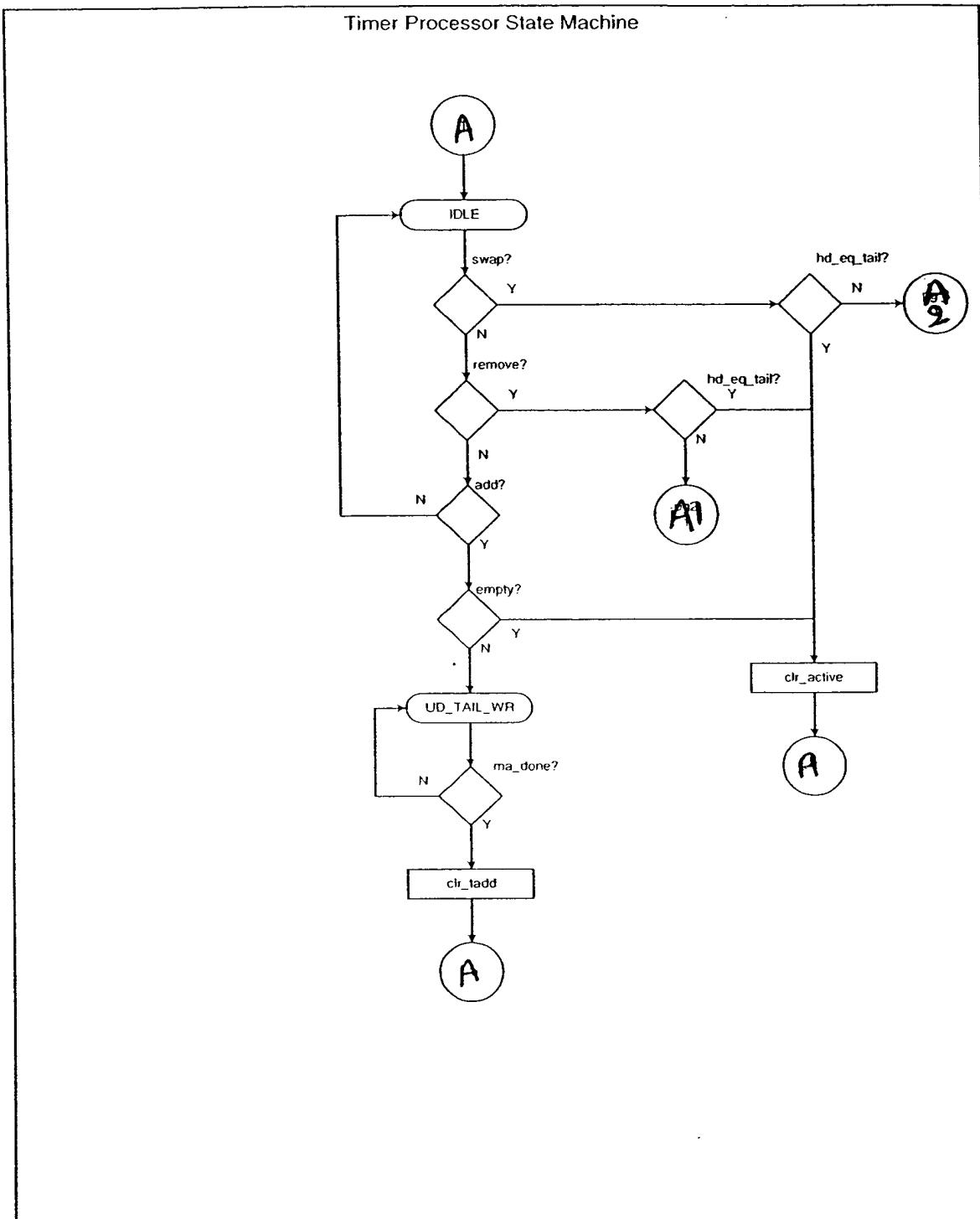


Figure 3L9A

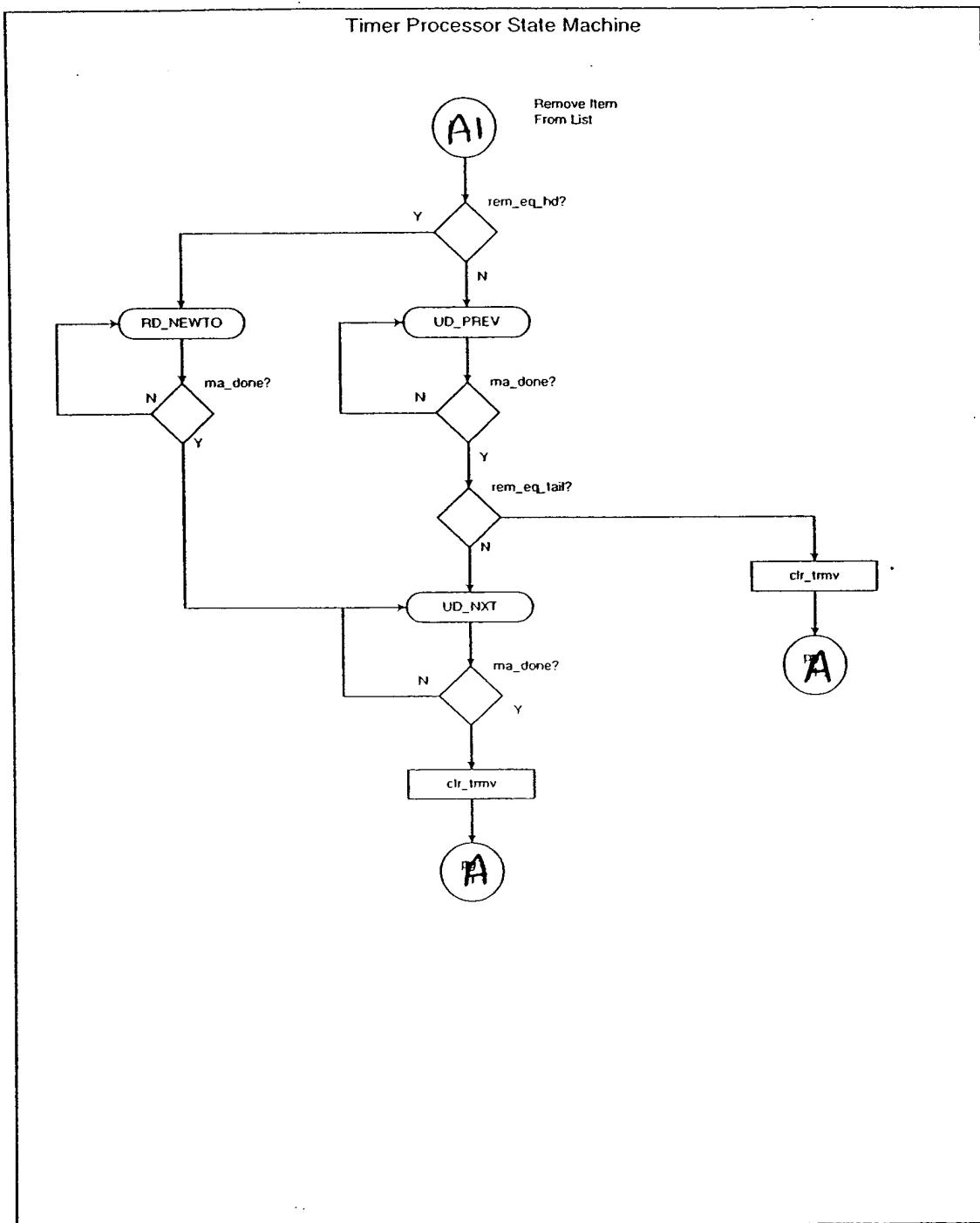


Figure 3L9B:

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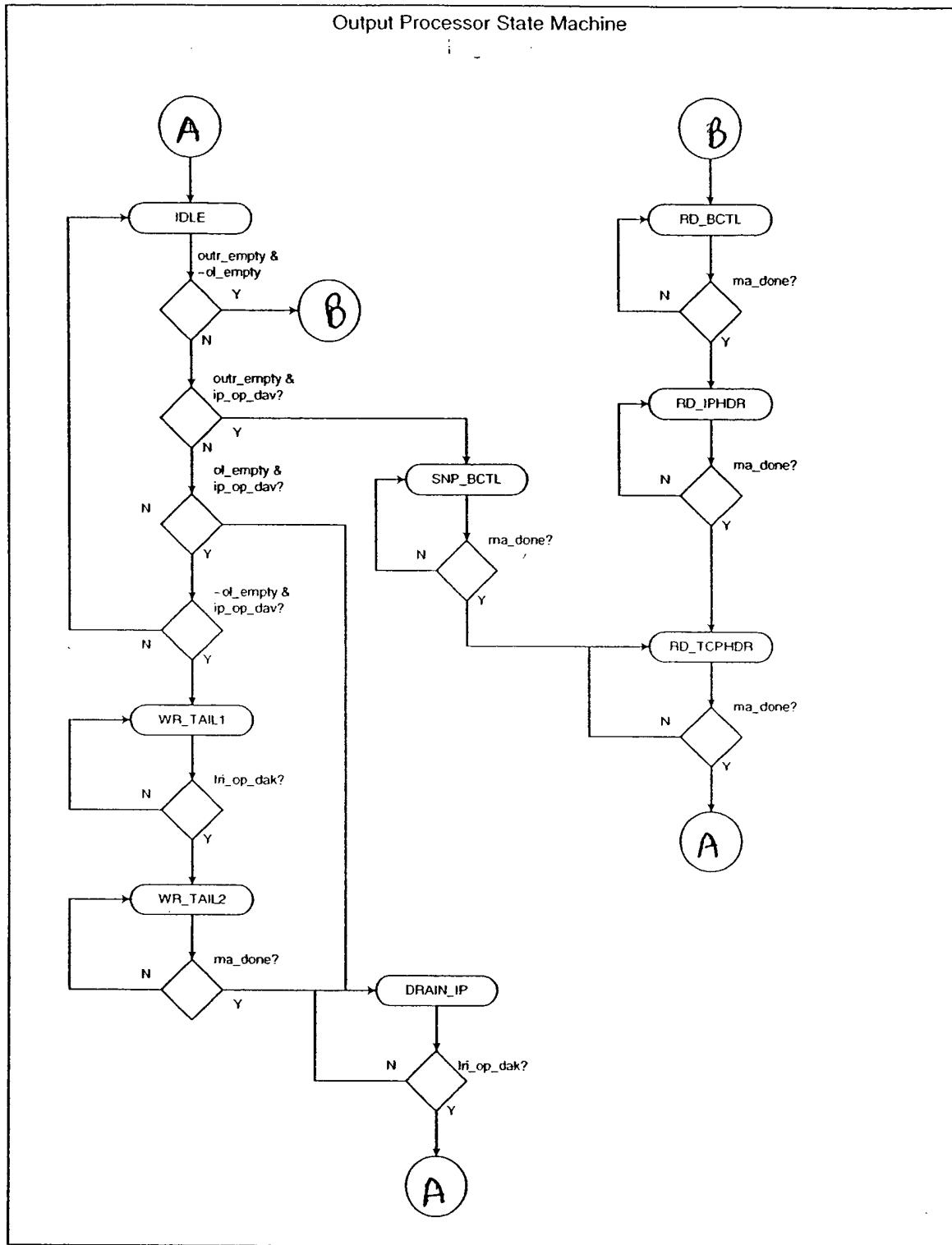


Figure 3L10 -

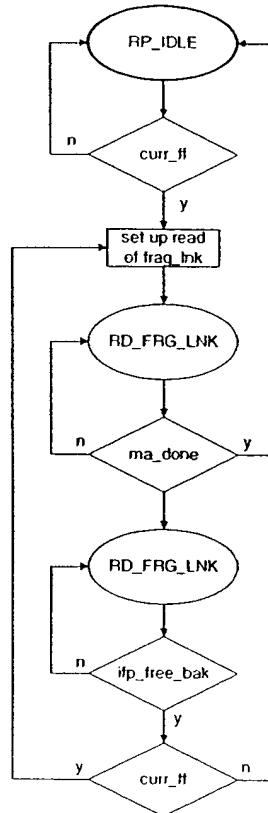


Figure 3L11 - Return Processor State Machine

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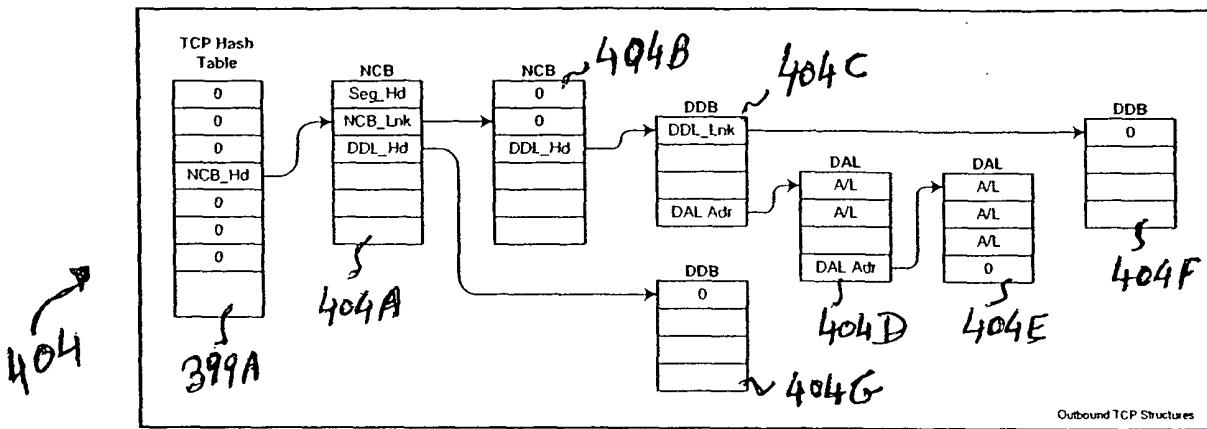


Figure 4A

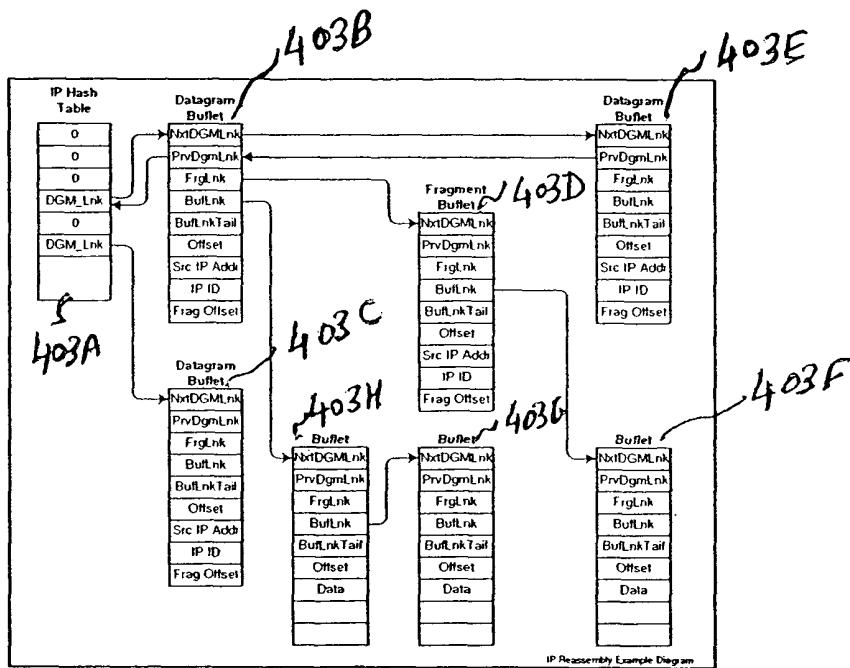


Figure 4B

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